

Communications

XRT5683A

PCM Line Interface Chip

Features

- Single 5V Supply
- Receiver Input Can Be Either Balanced or Unbalanced
- Up To 8.448Mbps Operation In Both Tx and Rx Directions
- TTL-Compatible Interface
- Device Can Be Used as a Line Interface Unit without Clock Recovery

Applications

- T1, T2, E1 & E2 Rates, PCM Line Interface
- Network Multiplexing and Terminating Equipment

The XRT5683A is a PCM line interface chip consisting of both transmit and receive circuitry. This device is offered in a plastic dual in-line (PDIP) or in a surface mount package (SOIC). The maximum bit rate of the chip is 8.448Mbps, and the signal level to the receiver can be attenuated by -10dB cable loss at one-half the bit rate. At nominal supply voltage operation, the typical current consumption is 40mA.