

# Communications

## XRT56L85

### Low Power PCM Line Interface

#### Features

- Low Power (Typical 14mA)
- Single +5V Supply
- Up to 2.048 Mbps Operation in Both TX and RX Directions
- Receiver Input can be:
  - Balanced Transformer Coupled
  - Capacitively (Twisted Pair)
  - Single Coaxial Capacitive Coupling

#### Applications

- T1 and CEPT Interfaces
- CPI
- DMI

The XRT56L85 is a PCM line interface chip. It consists of both transmit and receive circuitry in a DIL 18 pin package. The maximum bit rate the chip can handle is 2.048 Mbps and the signal level to the received can be attenuated by 10dB of cable loss at half the bit rate. Total current consumption is between 12-16mA at +5V.