

Communications

XRT7288

CEPT1 Line Interface Unit

Features

- Fully Integrated 2.048Mbps Line Interface
- Complies with CCITT Specifications G.793, G.823, I.431, G.732, G.735, G.739
- Pin Selectable 75 Ohm or 120 Ohm Operation
- Monolithic Clock Recovery
- Low Power Dissipation

The XRT7288 CEPT1 Line Interface is an integrated circuit that provides a 2.048 Mbits/s line interface to either twisted-pair or coaxial cable as specified in CCITT requirements G.703, G.823, I.431, G.732, and G.735 G.739. The device performs receive pulse regeneration, timing recovery, and transmit pulse driving functions. The XRT7288 device is manufactured by using low-power CMOS technology and is available in a 28-pin, plastic PDIP or in a 28-pin, plastic SOJ package for surface mounting. The XRT7288 device is functionally compatible with the LC1135B device.