

Communications

ST49C107A

Preprogrammed CPU Mother Board Frequency Generator

Features

- Provides Reference Clock And Synthesized Clock
- 5 to 32 MHz Input Reference Frequency
- Pin-to-Pin Compatible to Avasem AV9107
- Programmable Analog Phase Locked Loop
- Low Power Single 5V CMOS Technology
- Up to 16 Frequencies Stored Internally
- 8/14 pin DIP or SOIC Package

The ST49C107A-04 is a mask programmable monolithic analog CMOS device designed to generate two simultaneous clocks. The output frequency can vary from 2 to 130MHz, with up to 16 single selectable preprogrammed frequencies stored in internal ROM.

The ST49C107A-04 is designed to replace existing CPU mother board clocks generated from individual oscillators in order to reduce board space and number of oscillators. To provide high speed and low jitter clock, the parts utilize a high speed analog CMOS phase locked loop using 14.318 MHz system clock as the reference clock (note that reference clock can be changed to generate optional frequencies from a standard programmed device). The programmed clock outputs are selectable via four address lines.