

Communications

XRT6164A

Digital Line Interface Transceiver

Features

- Single 5V Supply
- Compatible with CCITT G.703 64Kbps Co-Directional Interface Recommendation When Used With Either XRT6165 or XRT6166
- Low Power
- Converts Balanced Bipolar Transmit and Receive Signals Propagated Over Two Twisted Pair Cables to TTL Compatible Dual-Rail Data
- Links Remote Equipment Equipped with CCITT G.703 64Kbps Co-Directional Interfaces Over Distances Up to 500 Meters Without Equalization
- Receive Data Comparator Threshold Storage Provides Ping-Pong Operation Capability
- Loss of Signal Alarm
- Dual Matched Driver Outputs

Applications

- Data Adaption Unit (DAU)
- General Purpose TTL Compatible Line Interface

The XRT6164A is a bipolar analog chip intended for general purpose line interface applications at bit rates up to 1.544Mbps (T1). It contains both receive and transmit circuitry in a 16 pin dual-in-line plastic package. The receiver is designed for short line applications having a cable loss up to 10dB measured at the half bit rate. The transmitter has open collector line driver outputs that are capable of handling up to 40mA.

When used in conjunction with either XRT6165 or XRT6166, the chip set provides a 64Kbps co-directional interface as specified in CCITT G.703.