FUJITSU

Corrections of Hardware Manual

MB90420/5 (A)-

HM90420_add_V100

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Addendum, MB90420-5 Hardware Manual (mb90420-5(A)-draftv1-4)

This is the Addendum for the Hardware Manual mb90420-5(A)-draftv1-4 of the MB90420/5 Microcontroller series. It describes all known discrepancies of the MB90420/5 Microcontroller series Hardware Manual.

Ref. Number (Text Link)	Date dd.mm.yy	Version No.	Chapter/Page	Description/Correction
HWM90420001	10.10.00	1.00	24.1	Wrong oscillation clock cycle count

Chapter 24: LOW VOLTAGE AND CPU OPERATION DETECTION RESET CIRCUIT

Table 24-2 incorrect.

OLD:

Table 24-2 Interval Time for CPU Operation Detection Reset Circuit

Interval Time	Oscillation Clock Cycle Count
About 262ms	2^{20} clock cycle

NEW:

Table 24-2 Interval Time for CPU Operation Detection Reset Circuit

Interval Time	Oscillation Clock Cycle Count
About 262ms	2^{19} clock cycle