



Corrections of Hardware Manual

MB90580 -

HM90580_add_V100

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Addendum, MB90580 Hardware Manual (CM44-10111-1E)

This is the Addendum for the Hardware Manual CM44-10111-1E of the MB90580 Microcontroller series. It describes all known discrepancies of the MB90580 Microcontroller series Hardware Manual.

Ref. Number (Text Link)	Date dd.mm.yy	Version No.	Chapter/Page	Description/Correction
HWM90580001	09.07.01	1.00	Chapter 17	A/D Converter: AVcc value must be same as Vcc
HWM90580002	09.07.01	1.00	Chapter 5	Release from Watch Mode
HWM90580003	09.07.01	1.00	Chapter 1.6	Voltage drop down

HWM90580001

Chapter 17: A/D Converter

If the A/D converter is used AVcc must have the same value as Vcc. Otherwise a Latchup could happen.

HWM90580002

Chapter 1.8 Precautions on Handling of Device

Watchmode

Condition: Watch Mode Subclock connected(normal operation) or Watchmode, No subclock connected

Description: If MCU has entered the Watch Mode and only the RST Reset signal is asserted, it could happen, that the CPU does not restart correctly.

Workaround: RST and HST reset must be asserted simultaneously. Also a power-on reset will restart the CPU correctly again.

HWM90580003

Chapter 1.8: Precautions on Handling of Device

Voltage Drop down

Condition: Voltage Drop on Vcc, No Subclock connected

Description: If no subclock is connected, it possibly may happen, that after a voltage drop on Vcc, the MCU does not restart correctly, even if RST and HST is asserted simultaneously.

Details: If a voltage drop on Vcc occurs, there is no power-on reset executed, if the voltage Vcc does not drop below under 0.2V for a certain time (toff), which is specified in the DS. See details on Vcc in the corresponding Datasheet. Normally, if HST & RST is asserted afterwards, the MCU would restart correctly. If no subclock is connected, it possibly may happen, that the CPU does not start/work correctly even after RST & HST reset.

Workaround:

- a) The usage of a Subclock is highly recommended. If a Subclock is connected and a RST & HST Reset is asserted (RST = HST, reset simultaneously) the CPU will restart correctly.
- b) Perform a correct power-on Reset (corresponding to Vcc timing specified in Datasheet)