



Corrections of Hardware Manual

MB91109 -

HM91109_add_V100

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Addendum, MB91109 Hardware Manual (CM71-10106-1E)

This is the Addendum for the Hardware Manual CM71-10106-1E of the MB91109 microcontroller series. It describes all known discrepancies of the MB91109 microcontroller series Hardware Manual.

| Ref. Number (Text Link) | Date dd.mm.yy | Version No. | Chapter/Page | Description/Correction |
|-----------------------------|------------------|----------------|--------------|---|
| HWM91109001 | 28.06.00 | V1.00 | Chapter 16.5 | Flash Memory Access, with 2 cycles at 25MHz |
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Chapter 16.5 Flash Memory Access Mode

FLASH on C-Bus can be accessed with 2 cycles of 25MHz clock. The bus width is 32bit and if instructions are sequentially fetched, each 16bit instruction can be executed at one 25MHz clock.