MOTOROLA

*Microprocessor and Memory
Technologies Group*

MC68322

Addendum to
MC68322 Integrated Printer
Processor User's Manual

July 1, 1994

The following replaces 12.7 BitBLT and Scanline Order Execution on page 12-15.

Tables 12-3 lists the bitBLT and scanline transfer timings based upon an MC68322 system running at 20 MHz with DRAM running at 3,2,1,2 bus cycles.

Table 12-3. BitBLT and Scanline Execution Times

Operands/Orders	Time (µs)
BitBLT Execution Time (32 × 32)	
D = f(S)	30.75
D = f(S,D)	30.75
D = f(H,D)	31.05
D = f(S,H,D)	40.65
D = f() Solid fill	21.00
BitBLT Execution Time (30 × 50)	
D = f(S)	48.90
D = f(S,D)	48.90
D = f(H,D)	47.10
D = f(S,H,D)	63.30
D = f() Solid fill	31.65
BitBLT Execution Time (100 × 100)	
D = f(S)	139.05
D = f(S,D)	156.30
D = f(H,D)	148.05
D = f(S,H,D)	189.45
D = f() Solid fill	93.50
Scanline Execution Time (30 × 50)	
D = f(SL,D)	70.05
D = f(SL,H,D)	195.90

SEMICONDUCTOR PRODUCT INFORMATION

Literature Distribution Centers:

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