

M68HC05PGMR-2/D
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M68HC05PGMR-2
PROGRAMMER
USER'S MANUAL

Third Edition

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Table of Contents

1. INTRODUCTION.....	1-5
1.1 BOARD LAYOUT AND DESCRIPTION.....	1-6
2. PROGRAMMING PROCEDURES	2-7
2.1 68HC805C4 PROGRAMMING AND VERIFYING.....	2-7
2.1.1 Programming Procedure.....	2-7
2.1.2 Notes on the Programming Operation.....	2-7
2.1.3 Verifying Procedure.....	2-8
2.1.4 Notes on the Programming Operation.....	2-8
2.2 68HC705C5 PROGRAMMING AND VERIFYING.....	2-9
2.2.1 Programming Procedure.....	2-9
2.2.2 Notes on the Programming Operation.....	2-9
2.2.3 Verifying Procedure.....	2-10
2.2.4 Notes on the Programming Operation.....	2-10
2.3 68HC705C8 PROGRAMMING AND VERIFYING.....	2-11
2.3.1 Programming Procedure.....	2-11
2.3.2 Notes on the Programming Operation.....	2-11
2.3.3 Verifying Procedure.....	2-12
2.3.4 Notes on the Verifying Operation.....	2-12
2.3.5 Secure EPROM Operation.....	2-13
2.4 68HC705C9/D9 PROGRAMMING AND VERIFYING.....	2-14
2.4.1 Programming Procedure.....	2-14
2.4.2 Notes on the Programming Operation.....	2-14
2.4.3 Verifying Procedure.....	2-15
2.4.4 Notes on the Verifying Operation.....	2-15

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CHAPTER 1.

INTRODUCTION

This manual describes the procedure used to program and verify an MC68HC(7/8)05C series microcontroller (MCU) on the M68HC05PGMR-2. All that is required to program the MCU is the PGMR, +5 volt and V_{PP} dc power supply, and a pre-programmed EPROM.

The PGMR provides programming for either 40 pin dual-in-line package (DIP) MCU device programming, or 44 pin plastic leaded chip carrier (PLCC) MCU device programming.

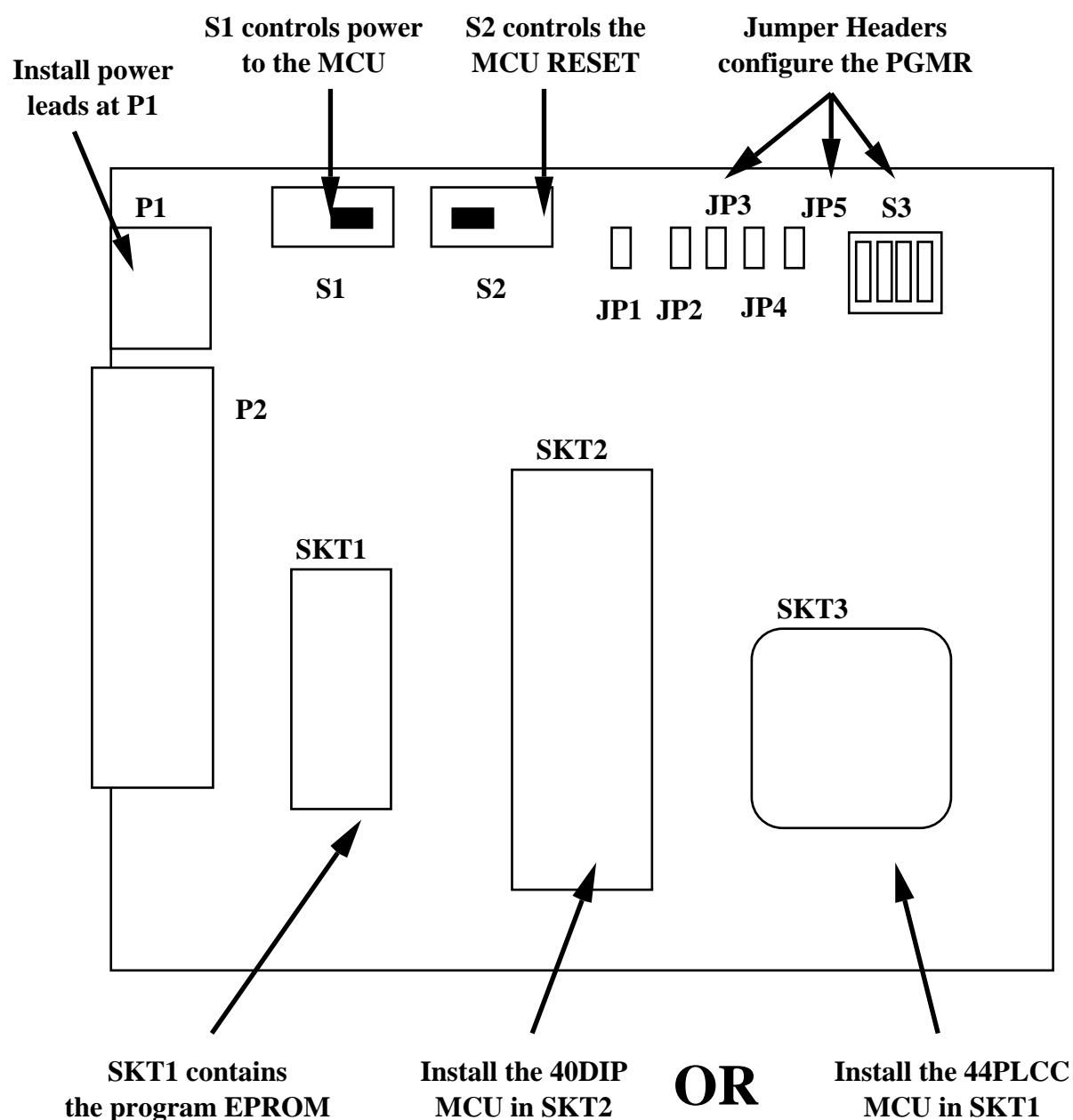


Figure 1.1 M68HC05PGMR-2 Layout

1.1 BOARD LAYOUT AND DESCRIPTION

Figure 1.1 shows the layout of the PGMR. As shown in the figure there are various controls, connectors and sockets that provide the ability to configure the board to specific MCU programming requirements.

For the board to function correctly you must supply the required power to the board at connector P1. +5V dc @ 100mA and ground lines are required. In addition a V_{PP} power supply is needed to perform the programming. The value of this supply may vary between MCUs, please refer to this manual and the device data book for further information.

Switches S1 and S2 control the activity of the PGMR. S1 is a simple on/off switch that applies power to the MCU device when on. Never remove or install an MCU when the S1 switch is in the ON position. S2 control the MCU reset line. Programming cannot take place while the part is in reset mode.

Five jumper headers (JP1 to JP5) configure the PGMR for the particular MCU and function in use. They select between different MCUs, EPROM sizes, and modes. The following sections describe how to configure these jumpers for your programming requirements.

Lastly, sockets SKT1 to SKT3 allow for the installation of the MCUs and the master EPROM. SKT1 is for installation of the EPROM that contains all the data being programmed into the MCU. SKT2 provides support for a 40 pin DIP package and SKT3 for a 44 pin PLCC package. Not all of the MCUs supported by this board may be available in each package. The SKT3 socket requires that the MCU is installed in a **top-down** or **dead-bug** fashion.

IMPORTANT NOTE

You must never install more than one MCU on this board at any time. That is there must never be an MCU installed in SKT2 and another installed in SKT3 simultaneously. Such a configuration may damage either of the MCUs and the board itself.

Always ensure that the devices are orientated correctly into their respective sockets.

CHAPTER 2.

PROGRAMMING PROCEDURES

The programmer copies a user program contained in an external EPROM, into the internal PROM (OTPROM/EPROM) or EEPROM (C4 only) of the MCU device

The following sections describe the procedure taken to program each MCU type.

2.1 68HC805C4 PROGRAMMING AND VERIFYING

There are two procedures used: one for programming and verifying and one that allows verifying only.

2.1.1 Programming Procedure

Settings required:

JP1	No Connection	JP2	C4/C8
JP3	C4/C8	JP4	27C64/27C128
JP5	PROG	S1	OFF
S2	IN	S3	OFF OFF OFF OFF
SKT1	27C64 EPROM Installed		

- a) Apply power to the PGMR board.
- b) Insert the 805C4 MCU device into the PGMR.
- c) Set S1 to ON.
- d) Set S2 to OUT. The selected programming routine is then executed.
- e) Once the programming is complete (see section 2.1.2), set S2 to IN and S1 to OFF. You may now remove the MCU from the socket.

2.1.2 Notes on the Programming Operation

In the program and verify MCU PROM routine, the contents of an external EPROM are copied into the MCU EEPROM. There is a direct correlation of addresses between the two devices (MCU and external EPROM). Non-MCU EEPROM addresses are ignored so data contained in those areas are not accessed. Unprogrammed EEPROM address locations should contain \$FF to speed up the programming operation. During the programming routine, the PROGRAM LED D6 is illuminated. At the end of the programming routine, D6 is turned off, and the verification routine is entered. If the contents of the MCU EEPROM and external EPROM exactly match, then the VERIFY LED D5 is illuminated.

During the verification routine, all locations are compared to the data residing in external EPROM. The verification routine will stop if a discrepancy has been detected and the error address location will be placed on the external memory address bus.

2.1.3 Verifying Procedure

Settings required:

JP1	No Connection	JP2	C4/C8
JP3	C4/C8	JP4	27C64/27C128
JP5	PROG	S1	OFF
S2	IN	S3	OFF ON OFF OFF
SKT1	27C64 EPROM Installed		

- a) Apply power to the PGMR board.
- b) Insert the 805C4 MCU device into the PGMR.
- c) Set S1 to ON.
- d) Set S2 to OUT. The selected verification routine is then executed.
- e) Once the verification is complete (see section 2.1.4), set S2 to IN and S1 to OFF. You may now remove the MCU from the socket.

2.1.4 Notes on the Programming Operation

The verify MCU EEPROM contents routine is normally entered automatically after the MCU EEPROM is programmed. Direct entry of this mode will cause the MCU EEPROM contents to be compared to external EPROM contents residing at the same address locations. Both D5 and D6 LEDs are turned off at this time until verification is completed. Upon completion of the verification routine (every location verified) the VERIFIED LED D5 is illuminated. If D5 does not illuminate, a discrepancy has been detected and the error address location will be placed on the external memory address bus.

NOTE

MCU EEPROM blank checking can be accomplished by placing \$FF into the locations of the external EPROM that map those of the MCU EEPROM. The above verify MCU routine will then verify that the device is blank.

2.2 68HC705C5 PROGRAMMING AND VERIFYING

There are two procedures used: one for programming and verifying and one that allows verifying only.

2.2.1 Programming Procedure

Settings required:

JP1	No Connection	JP2	C4/C8
JP3	C4/C8	JP4	27C64/27C128
JP5	PROG	S1	OFF
S2	IN	S3	ON OFF OFF OFF
SKT1	27C64 EPROM Installed		

- a) Apply power to the PGMR board.
- b) Insert the 705C5 MCU device into the PGMR.
- c) Set S1 to ON.
- d) Set S2 to OUT. The selected programming routine is then executed.
- e) Once the programming is complete (see section 2.1.2), set S2 to IN and S1 to OFF. You may now remove the MCU from the socket.

2.2.2 Notes on the Programming Operation

In the program and verify MCU EPROM routine, the contents of an external EPROM are copied into the MCU EPROM. There is a direct correlation of addresses between the two devices (MCU and external EPROM). Non-MCU EPROM addresses are ignored so data contained in those areas are not accessed. Unprogrammed EPROM address locations should contain \$00 to speed up the programming operation. During the programming routine, the PROGRAM LED D6 is illuminated. At the end of the programming routine, D6 is turned off, and the verification routine is entered. If the contents of the MCU EPROM and external EPROM exactly match, then the VERIFY LED D5 is illuminated.

During the verification routine, all locations are compared to the data residing in external EPROM. The verification routine will stop if a discrepancy has been detected and the error address location will be placed on the external memory address bus.

2.2.3 Verifying Procedure

Settings required:

JP1	No Connection	JP2	C4/C8
JP3	C4/C8	JP4	27C64/27C128
JP5	PROG	S1	OFF
S2	IN	S3	ON ON OFF OFF
SKT1	27C64 EPROM Installed		

- a) Apply power to the PGMR board.
- b) Insert the 705C5 MCU device into the PGMR.
- c) Set S1 to ON.
- d) Set S2 to OUT. The selected verification routine is then executed.
- e) Once the verification is complete (see section 2.2.4), set S2 to IN and S1 to OFF. You may now remove the MCU from the socket.

2.2.4 Notes on the Programming Operation

The verify MCU PROM contents routine is normally entered automatically after the MCU EPROM is programmed. Direct entry of this mode will cause the MCU EPROM contents to be compared to external EPROM contents residing at the same address locations. Both D5 and D6 LEDs are turned off at this time until verification is completed. Upon completion of the verification routine (every location verified) the VERIFIED LED D5 is illuminated. If D5 does not illuminate, a discrepancy has been detected and the error address location will be placed on the external memory address bus.

NOTE

MCU EPROM blank checking can be accomplished by placing \$00 into the locations of the external EPROM that map those of the MCU EPROM. The above verify MCU routine will then verify that the device is blank.

2.3 68HC705C8 PROGRAMMING AND VERIFYING

There are two procedures used: one for programming and verifying and one that allows verifying only. See section 2.3.5 for procedures used when securing the C8 EPROM.

2.3.1 Programming Procedure

Settings required:

JP1	No Connection	JP2	C4/C8
JP3	C4/C8	JP4	27C64/27C128
JP5	PROG	S1	OFF
S2	IN	S3	OFF OFF OFF OFF
SKT1	27C64 EPROM Installed		

- a) Apply power to the PGMR board.
- b) Insert the 705C8 MCU device into the PGMR.
- c) Set S1 to ON.
- d) Set S2 to OUT. The selected programming routine is then executed.
- e) Once the programming is complete (see section 2.1.2), set S2 to IN and S1 to OFF. You may now remove the MCU from the socket.

2.3.2 Notes on the Programming Operation

In the program and verify MCU EPROM routine, the contents of an external EPROM are copied into the MCU EPROM. There is a direct correlation of addresses between the two devices (MCU and external EPROM). Non-MCU EPROM addresses are ignored so data contained in those areas are not accessed. Unprogrammed EPROM address locations should contain \$00 to speed up the programming operation. During the programming routine, the PROGRAM LED D6 is illuminated. At the end of the programming routine, D6 is turned off, and the verification routine is entered. If the contents of the MCU EPROM and external EPROM exactly match, then the VERIFY LED D5 is illuminated.

During the verification routine, all locations are compared to the data residing in external EPROM. The verification routine will stop if a discrepancy has been detected and the error address location will be placed on the external memory address bus.

2.3.3 Verifying Procedure

Settings required:

JP1	No Connection	JP2	C4/C8
JP3	C4/C8	JP4	27C64/27C128
JP5	PROG	S1	OFF
S2	IN	S3	OFF ON OFF OFF
SKT1	27C64 EPROM Installed		

- a) Apply power to the PGMR board.
- b) Insert the 705C8 MCU device into the PGMR.
- c) Set S1 to ON.
- d) Set S2 to OUT. The selected verification routine is then executed.
- e) Once the verification is complete (see section 2.3.4), set S2 to IN and S1 to OFF. You may now remove the MCU from the socket.

2.3.4 Notes on the Verifying Operation

The verify MCU PROM contents routine is normally entered automatically after the MCU EPROM is programmed. Direct entry of this mode will cause the MCU EPROM contents to be compared to external EPROM contents residing at the same address locations. Both D5 and D6 LEDs are turned off at this time until verification is completed. Upon completion of the verification routine (every location verified) the VERIFIED LED D5 is illuminated. If D5 does not illuminate, a discrepancy has been detected and the error address location will be placed on the external memory address bus.

NOTE

MCU EPROM blank checking can be accomplished by placing \$00 into the locations of the external EPROM that map those of the MCU EPROM. The above verify MCU routine will then verify that the device is blank.

2.3.5 Secure EPROM Operation

The 705C8 contains a security bit that prevents reading of the internal EPROM. Use the secure PROM routines after you successfully program and verify the EPROM. This procedure only programs the SEC bit of the OPTION register (\$1FDF), but VPP is necessary. Once this bit is programmed, PROM is secure, and you cannot verify it.

No LED illuminates during this routine and no verification occurs. To ensure that security is properly enabled, attempt to perform another verify routine (2.3.3). If the green LED does not light, the EPROM is secure.

Settings required:

JP1	No Connection	JP2	C4/C8
JP3	C4/C8	JP4	27C64/27C128
JP5	PROG	S1	OFF
S2	IN	S3	OFF ON OFF ON
SKT1	27C64 EPROM Installed		

- a) Apply power to the PGMR board.
- b) Insert the 705C8 MCU device into the PGMR.
- c) Set S1 to ON.
- d) Set S2 to OUT. The selected programming routine executes.
- e) Once the programming is complete, set S2 to IN and S1 to OFF. You may now remove the MCU from the socket.

2.4 68HC705C9/D9 PROGRAMMING AND VERIFYING

There are two procedures used: one for programming and verifying and one that allows verifying only. Use the same procedure for a C9 or a D9.

2.4.1 Programming Procedure

Settings required:

JP1	No Connection	JP2	C9/D9
JP3	C9/D9	JP4	27C64/27C128 if 27C128 installed 27C256 if 27C256 installed
JP5	PROG	S1	OFF
S2	IN	S3	OFF OFF OFF OFF
SKT1	27C128 or 27C256 EPROM Installed		

- Apply power to the PGMR board.
- Insert the 705C9 or 705D9 MCU device into the PGMR.
- Set S1 to ON.
- Set S2 to OUT. The selected programming routine is then executed.
- Once the programming is complete (see section 2.4.2), set S2 to IN and S1 to OFF. You may now remove the MCU from the socket.

2.4.2 Notes on the Programming Operation

In the program and verify MCU EPROM routine, the contents of an external EPROM are copied into the MCU EPROM. There is a direct correlation of addresses between the two devices (MCU and external EPROM). Non-MCU EPROM addresses are ignored so data contained in those areas are not accessed. Unprogrammed EPROM address locations should contain \$00 to speed up the programming operation. During the programming routine, the PROGRAM LED D6 is illuminated. At the end of the programming routine, D6 is turned off, and the verification routine is entered. If the contents of the MCU EPROM and external EPROM exactly match, then the VERIFY LED D5 is illuminated.

During the verification routine, all locations are compared to the data residing in external EPROM. The verification routine will stop if a discrepancy has been detected and the error address location will be placed on the external memory address bus.

2.4.3 Verifying Procedure

Settings required:

JP1	No Connection	JP2	C4/C8
JP3	C9/D9	JP4	27C64/27C128 if 27C128 installed 27C256 if 27C256 installed
JP5	PROG	S1	OFF
S2	IN	S3	OFF ON OFF OFF
SKT1	27C128 or 27C256 EPROM Installed		

- a) Apply power to the PGMR board.
- b) Insert the 705C9 or 705D9 MCU device into the PGMR.
- c) Set S1 to ON.
- d) Set S2 to OUT. The selected verification routine is then executed.
- e) Once the verification is complete (see section 2.4.4), set S2 to IN and S1 to OFF. You may now remove the MCU from the socket.

2.4.4 Notes on the Verifying Operation

The verify MCU PROM contents routine is normally entered automatically after the MCU EPROM is programmed. Direct entry of this mode will cause the MCU EPROM contents to be compared to external EPROM contents residing at the same address locations. Both D5 and D6 LEDs are turned off at this time until verification is completed. Upon completion of the verification routine (every location verified) the VERIFIED LED D5 is illuminated. If D5 does not illuminate, a discrepancy has been detected and the error address location will be placed on the external memory address bus.

NOTE

MCU EPROM blank checking can be accomplished by placing \$00 into the locations of the external EPROM that map those of the MCU EPROM. The above verify MCU routine will then verify that the device is blank.

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