Mask Set Errata 1 68HC908RK2 8-Bit Microcontroller Unit

INTRODUCTION

This mask set errata provides information pertaining to the low-voltage inhibit applicable to this 68HC908RK2 MCU mask set device:

1K00J

MCU DEVICE MASK SET IDENTIFICATION

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 1K00J. Slight variations to the mask set identification code may result in an altered version number, for example 2K00J.

MCU DEVICE DATE CODES

Device markings indicate the week of manufacture and the mask set used. The date is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code "0022" indicates the 22nd week of the year 2000.

MCU DEVICE PART NUMBER PREFIXES

Some MCU samples and devices are marked with an SC or XC prefix. An SC prefix denotes special/custom device. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC prefix.

When contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.

Specifications and information herein are subject to change without notice.



LOW-VOLTAGE INHIBIT

The low-voltage inhibit (LVI) trip points, low-voltage reset (LVR) and low-voltage detection (LVD), have broader voltage spreads than expected, resulting in a loss of yield. To supply parts, Motorola has relaxed the LVI limits as follows:

	Min	Max
LVR	1.76 V	2.00 V
LVD	1.90 V	2.15 V

The LVD's limits are designed to be higher than those of the LVR. LVR and LVD voltages are taken as a ratio from the same resistor array, and both are compared against the same V_{BREF} (bandgap reference). Then, by nature, LVD has higher values than the LVR.

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