

## *Mask Set Errata 3*

# **68HC08AS20 8-Bit Microcontroller Unit**

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## **INTRODUCTION**

This mask set errata provides information pertaining to the BDLC applicable to these 68HC08AS20 MCU mask set devices:

- 0G39Y
- 2G39Y

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## **MCU DEVICE MASK SET IDENTIFICATION**

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 2G39Y. Slight variations to the mask set identification code may result in an altered version number, for example 3G39Y.

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## **MCU DEVICE DATE CODES**

Device markings indicate the week of manufacture and the mask set used. The data is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code "9115" indicates the 15th week of the year 1991.

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## **MCU DEVICE PART NUMBER PREFIXES**

Some MCU samples and devices are marked with an SC or XC prefix. An SC prefix denotes special/custom device. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC prefix.

*When contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.*

Specifications and information herein are subject to change without notice.



## BYTE DATA LINK CONTROLLER (BDLC)

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After placing the BDLC into 4X receive mode by configuring BCR2 to \$20, an idle bus longer than 70  $\mu$ s generates repeating false out-of-range flags in the BSVR (\$1C). The 4X mode incoming message is received correctly regardless of whether the BSVR \$1C flags are cleared.

**Software Workaround** – Service the BSVR interrupts to clear the BSVR \$1C invalid flags.

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