

MOTOROLA
SEMICONDUCTOR
TECHNICAL DATA

CPU08

Addendum to
CPU08
Central Processor Unit
Reference Manual

This addendum provides corrections to the *CPU08 Central Processor Unit Reference Manual*, Rev. 1 (Motorola document number CPU08RM/AD).

Page 6-18, correct the **Source Forms, Addressing Modes, Machine Code, and Cycles** table as follows:

From:

Source Forms	Addr Mode	Machine Code		HC08 Cycles
		Opcode	Operand(s)	
CBEQ <i>opr, rel</i>	DIR	31	dd rr	5
CBEQA <i>#opr, rel</i>	IMM	41	ii rr	4
CBEQX <i>#opr, rel</i>	IMM	51	ii rr	4
CBEQ <i>X+, rel</i>	IX+	71	rr	4
CBEQ <i>opr,X+, rel</i>	IX1+	61	ff rr	5
CBEQ <i>opr,SP, rel</i>	SP1	9E61	ff rr	5

To:

Source Forms	Addr Mode	Machine Code		HC08 Cycles
		Opcode	Operand(s)	
CBEQ <i>opr, rel</i>	DIR	31	dd rr	5
CBEQA <i>#opr, rel</i>	IMM	41	ii rr	4
CBEQX <i>#opr, rel</i>	IMM	51	ii rr	4
CBEQ <i>X+, rel</i>	IX+	71	rr	4
CBEQ <i>opr,X+, rel</i>	IX1+	61	ff rr	5
CBEQ <i>opr,SP, rel</i>	SP1	9E61	ff rr	6



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Specifications and information herein are subject to change without notice.



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Page 6-20, correct the **Source Forms, Addressing Modes, Machine Code, and Cycles** table as follows:

From:

Source Forms	Addr Mode	Machine Code			HC08 Cycles
		Opcode	Operand(s)		
CBEQ <i>opr, rel</i>	DIR	31	dd	rr	5
CBEQA <i>#opr, rel</i>	IMM	41	ii	rr	4
CBEQX <i>#opr, rel</i>	IMM	51	ii	rr	4
CBEQ <i>X+, rel</i>	IX+	71	rr		4
CBEQ <i>opr, X+, rel</i>	IX1+	61	ff	rr	5
CBEQ <i>opr, SP, rel</i>	SP1	9E61	ff	rr	5

To:

Source Forms	Addr Mode	Machine Code			HC08 Cycles
		Opcode	Operand(s)		
CBEQ <i>opr, rel</i>	DIR	31	dd	rr	5
CBEQA <i>#opr, rel</i>	IMM	41	ii	rr	4
CBEQX <i>#opr, rel</i>	IMM	51	ii	rr	4
CBEQ <i>X+, rel</i>	IX+	71	rr		4
CBEQ <i>opr, X+, rel</i>	IX1+	61	ff	rr	5
CBEQ <i>opr, SP, rel</i>	SP1	9E61	ff	rr	6



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Page 6-28, correct the **Source Forms, Addressing Modes, Machine Code, and Cycles** table as follows:

From:

Source Forms	Addr Mode	Machine Code			HC08 Cycles
		Opcode	Operand(s)		
DBNZ <i>opr</i>	DIR	3B	dd	rr	5
DBNZA <i>opr</i>	INH	4B	rr		3
DBNZX <i>opr</i>	INH	5B	rr		3
DBNZ <i>opr</i>	IX	7B	rr		4
DBNZ <i>opr</i>	IX1	6B	ff	rr	5
DBNZ <i>opr</i>	SP1	9E6B	ff	rr	6

To:

Source Forms	Addr Mode	Machine Code			HC08 Cycles
		Opcode	Operand(s)		
DBNZ <i>opr, rel</i>	DIR	3B	dd	rr	5
DBNZA <i>rel</i>	INH	4B	rr		3
DBNZX <i>rel</i>	INH	5B	rr		3
DBNZ <i>X, rel</i>	IX	7B	rr		4
DBNZ <i>opr, X, rel</i>	IX1	6B	ff	rr	5
DBNZ <i>opr, SP, rel</i>	SP1	9E6B	ff	rr	6



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Page 6-38, correct the **Source Forms, Addressing Modes, Machine Code, and Cycles** table as follows:

From:


Source Forms	Addr Mode	Machine Code		HC08 Cycles
		Opcode	Operand(s)	
MOV <i>opr</i>	IMD	6E	ii dd	4
MOV <i>opr</i>	DD	4E	dd dd	5
MOV <i>opr</i>	IX+D	7E	dd	4
MOV <i>opr</i>	DIX+	5E	dd	4

To:

Source Forms	Addr Mode	Machine Code		HC08 Cycles
		Opcode	Operand(s)	
MOV <i>#opr, opr</i>	IMD	6E	ii dd	4
MOV <i>opr, opr</i>	DD	4E	dd dd	5
MOV <i>X+, opr</i>	IX+D	7E	dd	4
MOV <i>opr, X+</i>	DIX+	5E	dd	4



(Cut out the table labeled "To" and paste over existing table.)

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