

Mask Set Errata 3

68HC705P6 8-Bit Microcontroller Unit

INTRODUCTION

This errata provides information pertaining to the mask option register applicable to the following 68HC705P6 MCU mask set devices:

- 0E98K

MCU DEVICE MASK SET IDENTIFICATION

The mask set is identified by a four-character code consisting of a letter, two numerical digits, and a letter (e.g., E98K). Slight variations to the mask set identification code may result in an optional numerical digit preceding the standard four-character code (e.g., 0E98K).

MCU DEVICE DATE CODES

Device markings indicate the week of manufacture and the mask set used. The data is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. The date code "9115" would indicate the 15th week of the year 1991.

MCU DEVICE PART NUMBER PREFIXES

Some MCU samples and devices are marked with an "SC" or "XC" prefix. An "SC" prefix denotes special/custom device. An "XC" prefix denotes device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the "MC" prefix.

Whenever contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.


Specifications and information herein are subject to change without notice.



MASK OPTION REGISTER (MOR)

Programming time for the MOR byte (\$1F00) is longer than that for the other EPROM cells. Therefore, to ensure appropriate programming of the MOR, it is recommended that the MOR byte be programmed at least twice, but no more than three times with $V_{PP} = 17.5$ volts.

The RC bit of the MOR (bit 6) is hard-wired to the crystal oscillator configuration. No attempts should be made to set this bit to the RC configuration, as this option is currently unavailable.

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