Mask Set Errata 2

68HC805K3 8-Bit Microcontroller Unit

INTRODUCTION

This errata provides information pertaining to the Stop I_{DD} applicable to this 68HC805K3 MCU mask set device:

- 0H55F

MCU DEVICE MASK SET IDENTIFICATION

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 0H55F. Slight variations to the mask set identification code may result in an altered version number, for example 1H55F.

MCU DEVICE DATE CODES

Device markings indicate the week of manufacture and the mask set used. The data is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. The date code “9115” would indicate the 15th week of the year 1991.

MCU DEVICE PART NUMBER PREFIXES

Some MCU samples and devices are marked with an “SC” or “XC” prefix. An “SC” prefix denotes special/custom device. An “XC” prefix denotes device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the “MC” prefix.

When contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.

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STOP I_{DD}

The 68HC805K3 exhibits a high Stop I_{DD}. The magnitude of the current depends on the application. The Stop current is directly related to each programmed mask option register (MOR) bit. When V_{DD} = 5.5 V, each programmed MOR bit produces approximately 100 μA of Stop current. When V_{DD} = 3.0 V, each programmed MOR bit produces approximately 15 μA of Stop current.

Additional mask set erratas can be found on the World Wide Web at http://Design-NET.com/csic/TECHSPRT/TechSprt.htm.