Mask Set Errata 1

68HC708LN56 8-Bit Microcontroller Unit

INTRODUCTION

This mask set errata provides information pertaining to the system integration module (SIM) applicable to these 68HC708LN56 MCU mask set devices:

- 0G73N, 1G73N, 2G73N, 3G73N, 4G73N
- 0H40F, 1H40F

MCU DEVICE MASK SET IDENTIFICATION

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 0G73N. Slight variations to the mask set identification code may result in an altered version number, for example 1G73N.

MCU DEVICE DATE CODES

Device markings indicate the week of manufacture and the mask set used. The data is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code “9115” indicates the 15th week of the year 1991.

MCU DEVICE PART NUMBER PREFIXES

Some MCU samples and devices are marked with an SC or XC prefix. An SC prefix denotes special/custom device. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC prefix.

Whenever contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.

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When STOP is disabled, the STOP instruction is treated as an illegal opcode. If an illegal opcode is executed, the 68HC708LN56 will pull an internal reset that can also be seen externally on the $\overline{\text{RESET}}$ pin.

Failures occurred when parts were subjected to 85% humidity at 85 °C for 48 hours. If the STOP instruction is executed on the failing parts when STOP is disabled, the 68HC708LN56 will pull an internal reset. However, the external $\overline{\text{RESET}}$ pin does not get pulled.

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