

MC68HC11ED0 DEVICE INFORMATION
(Rev. 1 -10/29/93)
D45R Mask Sets

The following contains information and errata that pertains to D45R mask set device.

ERRATA

The Timer Overflow Flag in the TGLG2 register does not operate correctly when a Prescaler factor of 4, 8, or 16 is selected. Instead of setting at the \$FFFF - \$0000 transition of the Timer Counter Register (TCNT) the TOF also sets at intermediate transitions of the TCNT. The TOF operates correctly when a Prescaler factor of 1 is selected.

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