

XC68HC711E9 DEVICE INFORMATION
(Rev. 2 -12/8/92)
C47M Mask Set

The MC68HC711E9 EPROM based microcontroller is functionally equivalent to the MC68HC11E9 ROM based MCU. The MC68HC711E9 can also be used to emulate the MC68HC11A8 MCU. The following contains information and errata that pertains to C47M mask set devices.

USER INFORMATION

The user can program the 12K EPROM using the M68HC711E9PGMR Programmer Board or a modified M68HC11EVM Evaluation Module. Motorola sales offices and representatives have brochures describing these development tools.

The coplanarity of the J-leads on the ceramic quad prototype package is relaxed to 10 mils due to additional handling. This should have no effect in socketed applications, but could affect surface mounted (soldered) applications.

ERRATA

The CPU will not exit STOP mode correctly when interrupted by IRQ or XIRQ if the instruction immediately preceding STOP is a column 4 or 5 accumulator inherent (opcodes \$4X and \$5X) instruction, such as NEGA, NEGB, COMA, COMB, etc. These single byte, two cycle instructions must be followed by a NOP, then the STOP command. If reset is used to exit STOP mode, the CPU will respond correctly.

The address strobe trailing edge spec, tASED, is redefined as 15ns at 3MHZ. Note this signal is typically used in expanded mode operation only.

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