XC68HC711KA4 DEVICE INFORMATION
0D61N Mask Set

1.0 USAGE RECOMMENDATION

This device is an obsolete design and will be discontinued to be replaced by the MC68HC711KS2 device.

DETAIL

End of life announcement is presently schedule to occur 1st quarter, 1999.

2.0 QUALIFICATION STATUS

Reliability testing for this die allows XC status only, this being a prototype level not suitable for volume production.

DETAIL

-Die qual failed temp cycle at over 500 cycles in 68LD PLCC package -Reliability testing in packages other than 68LD PLCC has not been performed -Additional reliability testing will not be performed on this device. -Thorough functional characterization has not been performed; weakness beyond those disclosed may exist.

3.0 EPROM PERFORMANCE

EPROM SENSE AMP does not reliably sense erased cells (ones) at -40C

All product must receive 100% cold test at the minimum temperature for the device concerned This adds to product cost and delivery time requirements. Additionally, becuase cold temp performance is guaranteed by test instead of by design, there will be some PPM cold fail risk in any product when compared with devices with robust sense amp design.

4.0 INCORRECT INTERRUPT VECTOR FETCHES

Two nearly simultaneous interupts may not be properly handled by the internal interupt processing circuitry.

This may lead to an incorrect interupt vector being produced by the CPU. This is frequently a vector in RreservedS space.

WORK AROUND:

Good practice dictates initializing all unused vectors. For this problem, it is recommended that all reserved vectors also be initialized to an interrupt handling routine in the userUs software. The interupt handling routine in the userUs software for the reserved interrupts can be as simple as an RTI. The true pending interupts will then be serviced normally and correclty.

5.0 WINDOW CERQUAD PACKAGE CO-PLANARITY (68LD)

Lead co-planarity for this package will meet a 10 mil variation instead of the published specification of 6 mil maximum variation.

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