

MC68HC711E20 DEVICE INFORMATION

Rev.001

3G59F Mask Set

1.0 ANALOG TO DIGITAL CONVERTER

1.1 This device will not meet 2MHZ +/- 1 LSB spec.

All MC68HC711E20 devices will be tested to meet the published 3MHZ specification. This applies to all frequencies of operation.

DETAIL

The A2D has about a + 1/2 LSB offset from the ideal condition.

2.0 CONFIG; ROMON function 2.1 ROMON function follows CONFIG value In normal HC11 function, in single chip mode, ROMON is forced to one to enable EPROM regardless of the state of the ROMON bit in the CONFIG register. For the 3G59F mask set, if ROMON is programmed 0, EPROM will continue to be disabled from the memory map in single chip mode. Function follows the CONFIG register value.

WORKAROUND

Always set CONFIG value to reflect desired operation. If EPROM code is required in single chip operation then ROMON should be programmed as 1.

3.0 WINDOW CERQUAD PACKAGE CO-PLANARITY (52LD)

Lead co-planarity for this package will meet a 10 mil variation instead of the published specification of 6 mil maximum variation.

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