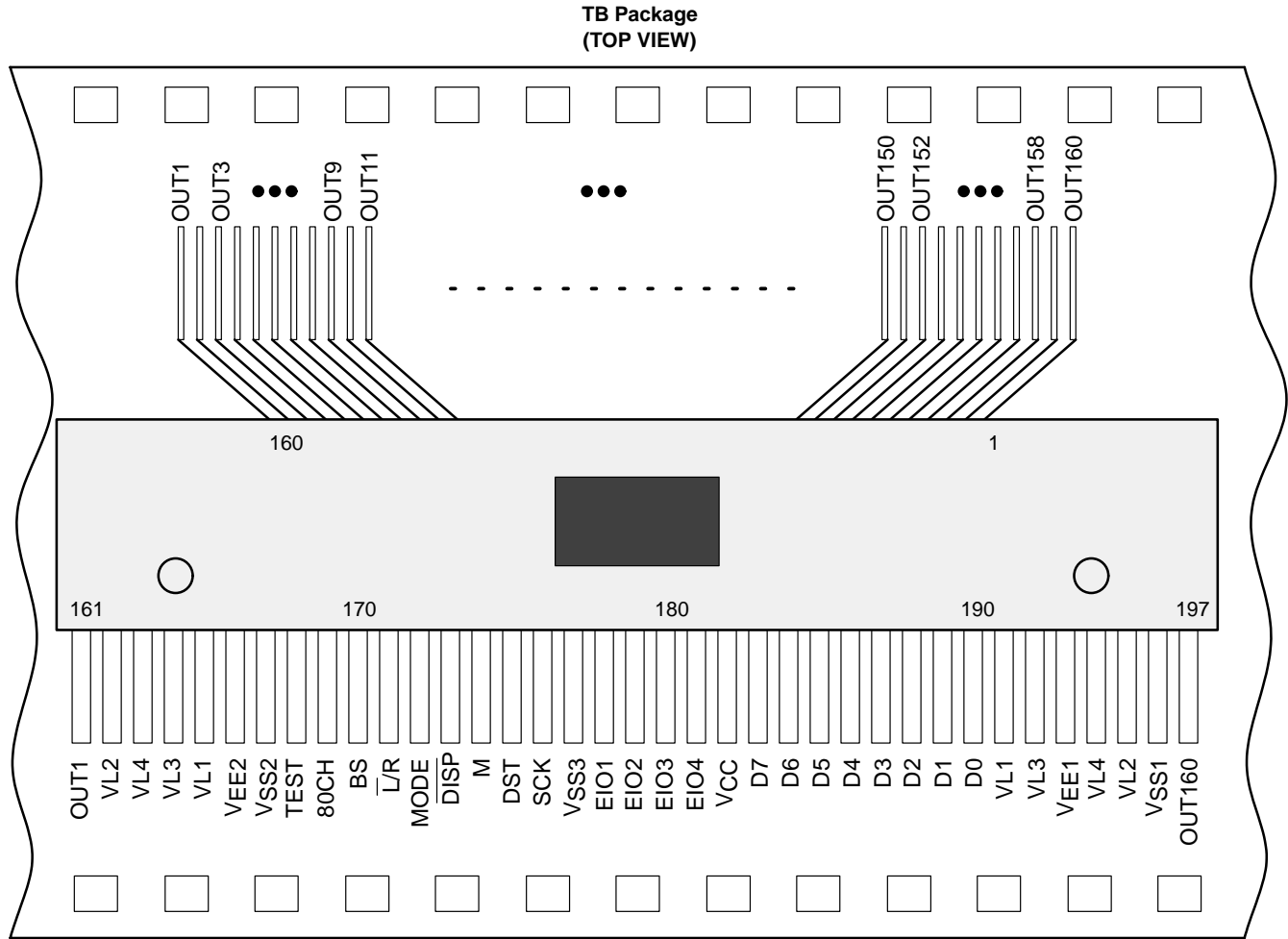


SNJ557202E 160-CHANNEL LCD DRIVER FOR DOT MATRIX STN

SGLS044 – JANUARY 1992

- One Chip for Both Common (Row) and Segment (Column) Driving
- Duty Cycle for LCD Driver Is Over 1/400
- Recommended V_{EE} Voltage Range for LCD Driver: 20 V to 42 V (45 V Max)
- 160 Channel Outputs
- 4-Bit or 8-Bit Data Bus
- 8-MHz Data Transfer Clock
- Power Supply Voltage: 5 V \pm 10%
- High-Voltage CMOS Si Gate Technology
- TAB (Tape Automated Bonding) Packaging



PRODUCT PREVIEW

description

The SNJ557202E is a CMOS integrated circuit designed to drive an LCD (liquid crystal display) for a dot matrix STN (super twisted nematic). The common- (row) and segment- (column) driver functions are integrated on a single chip.

In the common-driver mode, the outputs can be configured as one set of 160 output channels or as 2 sets of 80 output channels. In the segment-driver mode, the device can be configured for either four-bit-wide or eight-bit-wide data. The duty cycle is over 1/400, and the output bias voltage range (V_{EE}) is from 20 V to 42 V (45 V maximum).

This high-voltage CMOS Si gate device is available in a 197-pin TAB (tape automated bonding) package.

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