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- Four Independent Receivers With Common Enable Input
- High Input Sensitivity . . . 25 mV Max
- High Input Impedance
- MC3450 and MC3550 Have 3-State Outputs
- MC3452 Has Open-Collector Outputs
- Glitch-Free Power-Up/Power-Down Operation

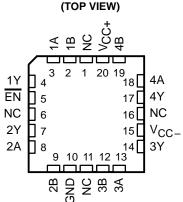
description

The MC3450, MC3550, MC3452, and MC3552 are quad differential line receivers designed for use in balanced and unbalanced digital data transmission. The MC34/3550 and MC34/3552 are the same except that the MC3450 and MC3550 have 3-state outputs whereas the MC3452 and MC3552 have open-collector outputs, which permit the wire-AND function with similar output devices. The 3-state and open-collector outputs permit connection directly to a bus-organized system.

The MC3450, MC3550, MC3452, and MC3550 are designed for optimum performance when used with either the MC3453 or MC3553 quad differential line driver or SN75109A, SN75110A, and SN75112 dual differential drivers.

The MC3450 and MC3452 are characterized for operation from 0°C to 70°C. The MC3550 and MC3552 are characterized for operation over the full military temperature range of -55°C to 125°C.

MC3450, MC3452 D OR N PACKAGE MC3550, MC3552 J PACKAGE (TOP VIEW)									
3	1 2 3 4 5 6 7 8	16 15 14 13 12 11 10 9	V _{CC+} 4B 4A 4Y V _{CC-} 3Y 3A 3B						
MC3550, MC	3552		PACKAGE						



NC-No internal connection

THE MC3452 IS NOT RECOMMENDED FOR NEW DESIGN

DIFFERENTIAL INPUTS A – B	ENABLE EN	OUTPUT Y							
$V_{ID} \ge 25 \text{ mV}$	L	Н							
–25 mV < V _{ID} < 25 mV	L	?							
$V_{ID} \le 25 \text{ mV}$	L	L							
Х	Н	Z							

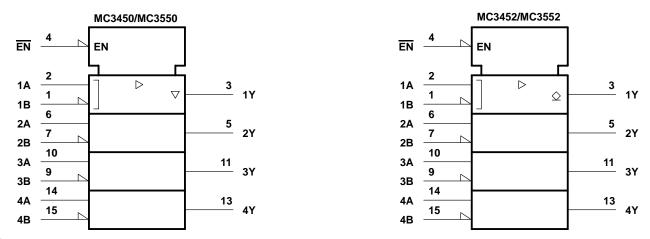
FUNCTION TABLE

H = high level, L = low level, ? = indeterminate, X = impedance (off)



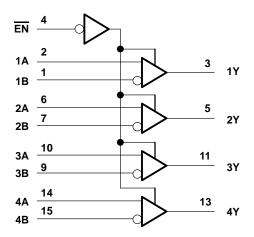
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logic symbols[†]



[†] These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

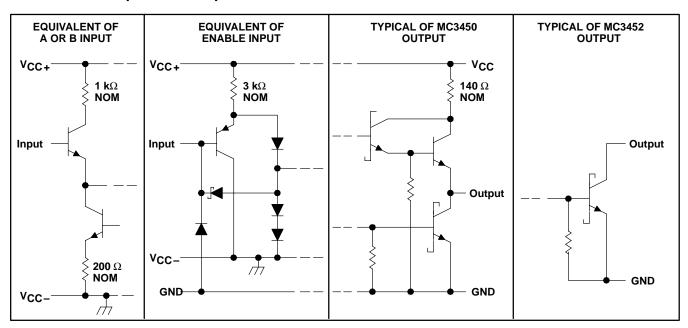
logic diagram (positive logic)





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schematics of inputs and outputs



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC+} (see Note 1) Supply voltage, V _{CC-} Differential input voltage (see Note 2) Common-mode input voltage (see Note 3) Enable input voltage Continuous total dissipation Storage temperature range Storage temperature for 60 seconds: FK package Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D or N package Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: J package	7 V ±6 V ±5 V 55 V ssipation Rating Table 0°C to 70°C 65°C to 150°C 260°C 260°C
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NOTES: 1. All voltage values, except differential input voltage, are with respect to network ground terminal.

2. Differential input voltage is measured at the noninverting input with respect to the corresponding inverting input.

3. Common-mode input voltage is the average of the voltages at the A and B inputs.

DISSIPATION RATING TABLE									
PACKAGE	T _A ≤ 25°C POWER RATING	OPERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING	T _A = 125°C POWER RATING					
D	950 mW	7.6 mW/°C	608 mW	_					
FK	1375 mW	11.0 mW/°C	880 mW	275 mW					
J	1375 mW	11.0 mW/°C	880 mW	275 mW					
N	1150 mW	9.2 mW/°C	736 mW	—					



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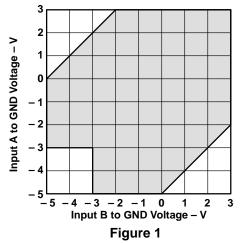
recommended operating conditions

		MI	N NOM	MAX	UNIT
	$T_A \ge 25^{\circ}C$	4	5 5	5 5.5	V
Supply voltage, V_{CC+}	T _A < 25°C	4.7	5 5	5 5.5	v
	$T_{A} \ge 25^{\circ}C$	-4	5 –5	5 –5.5	v
Supply voltage, V _{CC} _	T _A < 25°C	-4.7	5 –5	5 -5.5	v
High-level enable input voltage, VIH					V
Low-level enable input voltage, VIL				0.8	V
Low-level output current, IOL				-16	mA
Differential input voltage, VID (see Note 4)			†	5	V
Common-mode input voltage, VIC (see Note 4)		-3	†	3	V
Input voltage range, any different input to GND				3	V
Operating free-air temperature, T_{Δ}	MC3450, MC3452		0	70	°C
Operating nee-an temperature, 1A	MC3550, MC3552	-5	5	125	C

[†] The algebraic convention, in which the less positive (more negative) limit is designated minimum, is used in this data sheet for common-mode input voltage.

NOTE 4: The recommended combinations of input voltages fall within the shaded area of Figure 1.

RECOMMENDED COMBINATIONS OF INPUT VOLTAGES





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electrical characteristics over recommended operating free-air temperature range, $V_{CC\pm}$ = MAX (unless otherwise noted)

	PARAMETER TEST CONDITIONS		MC3	MC3450, MC3550			MC3452, MC3552				
	PARAMETER		TEST CO	NDITIONS	MIN	TYP†	MAX	MIN	TYP†	MAX	UNIT
VOH	High-level outpu	t voltage									V
V _{OL}	OL Low-level output voltage		$\frac{V_{CC}\pm}{EN \text{ at } 2 \text{ V,}}$ V _{IC} = -3 V to 3 V	V _{ID} = -25 mV, I _{OL} = 16 mA,			0.5			0.5	V
ЮН	High-level outpu	t current	$V_{CC\pm} = \pm 4.75$ V,	V _{OH} = 5.25 V						250	μA
		A inputs	$V_{ID} = -2 V$			30	75		30	75	μA
	High-level	B inputs	$V_{ID} = -2 V$			30	75		30	75	μΑ
ΊΗ	input current Low-level	EN	V _{IH} = 2.4 V				40			40	μΑ
		EN	V _{IH} = 5.25 V				1			1	mA
		A inputs	V _{ID} = 2 V				-10			-10	μA
ΙIL	Low-level	B inputs	V _{ID} = 2 V				-10			-10	μΑ
	input ouriont	EN	V _{IL} = 0.4 V				-1.6			-1.6	mA
107	High-impedance	state	V _O = 2.4 V				40				μA
loz	output current		V _O = 0.4 V				-40				μΑ
IOS	Short-circuit output current‡		V _{ID} = 25 mV, EN at 0.8 V	$V_{O} = 0,$	-18		-70				mA
ICCH+	Supply current fro	om V _{CC+} ,	<u>A inputs at GND,</u>	B inputs at 3 V,			60			60	mA
ICCH-	Supply current fro outputs high	om V _{CC-} ,	EN at 3 V				- 30			-30	mA

[†] All typical values are at $V_{CC+} = 5 \text{ V}$, $V_{CC-} = -5 \text{ V}$, $T_A = 25^{\circ}\text{C}$. [‡] Not more than one output should be shorted at a time.

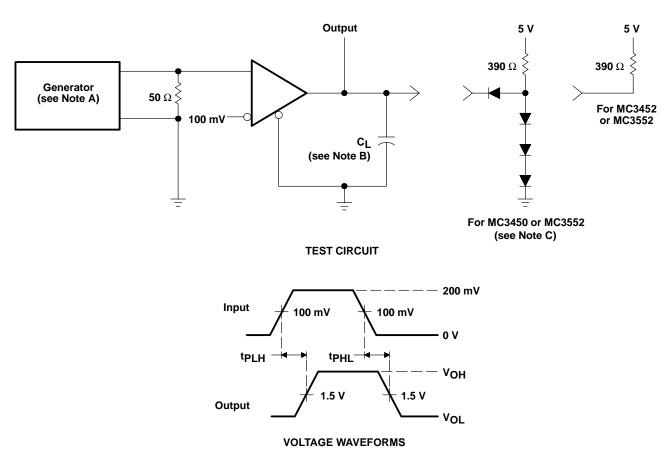
switching characteristics, V_{CC\pm} – ± 5 V, T_A = 25°C

PARAMETER	FROM	то	TEST CO	TEST CONDITIONS MC3450, MC			3550	MC3452, MC3552			
PARAMETER	(INPUT)	(OUTPUT)	1231 00	TEST CONDITIONS		TYP†	MAX	MIN	TYP†	MAX	UNIT
t=	A and B	Y	C _L = 50 pF,	See Figure 2		17	25				ns
^t PLH	A anu b	T	C _L = 15 pF,	See Figure 2					19	25	115
	A and B	Y	C _L = 50 pF,	See Figure 2		17	25				ns
^t PHL	A anu b	T	C _L = 15 pF,	See Figure 2					19	25	115
^t PZH	EN	Y	C _I = 50 pF,	F, See Figure 2			21				
^t PZL	EN	Y	ο <u> </u>	See Figure 2			27				ns
^t PHZ	EN	Y	C 15 pE	See Figure 3			18				ns
^t PLZ	EN	Y	C _L = 15 pF,	See Figure 5			29				115
^t PLH	EN	Y	C _L = 15 pF,	See Figure 4						25	ns
^t PHL	EN	Y	C _L = 15 pF,	See Figure 4						25	ns

[†] All typical values are at V_{CC+} = 5 V, V_{CC-} = –5 V, T_A = 25°C.



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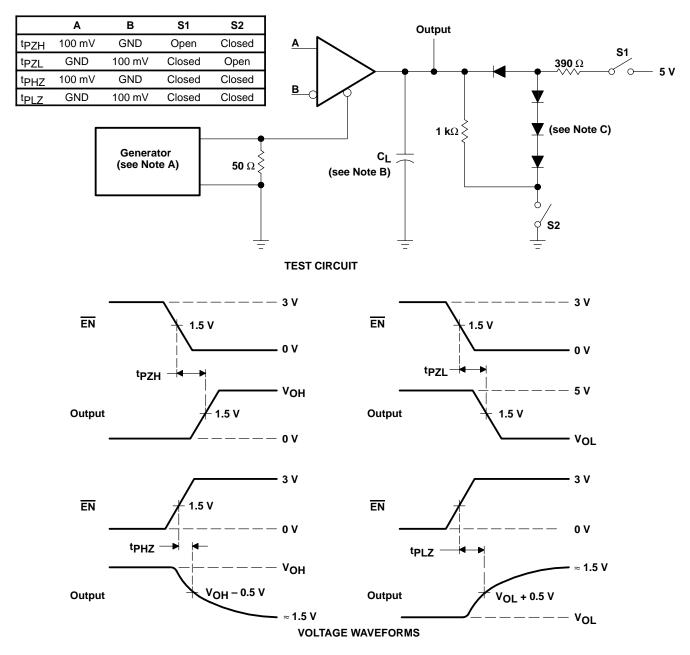
PARAMETER MEASUREMENT INFORMATION

- NOTES: A. The input pulse is supplied by a generator having the following characteristics: PRR \leq 1 MHz, duty cycle = 50%, t_f \leq 6 ns, t_f \leq 6 ns.
 - B. \dot{C}_L includes probe and jig capacitance.
 - C. All diodes are 1N916 or equivalent.

Figure 2. Test Circuit and Voltage Waveforms



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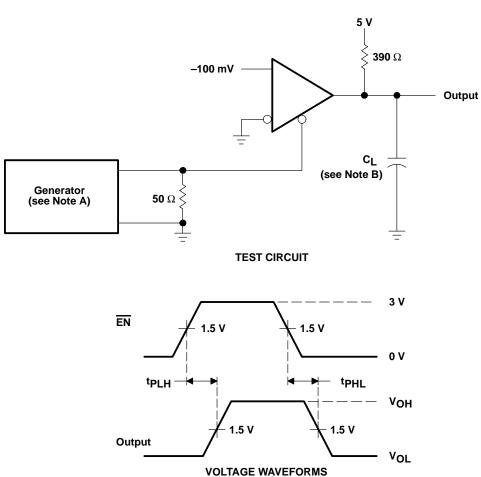
PARAMETER MEASUREMENT INFORMATION

- NOTES: A. The input pulse is supplied by a generator having the following characteristics: PRR \leq 1 MHz, duty cycle = 50%, t_f \leq 6 ns. t_f \leq 6 ns. B. CL includes probe and jig capacitance.
 - C. All diodes are 1N916 or equivalent.

Figure 3. MC3450 and MC3550 Test Circuit and Voltage Waveforms



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PARAMETER MEASUREMENT INFORMATION

NOTES: A. The input pulse is supplied by a generator having the following characteristics: PRR \leq 1 MHz, duty cycle = 50%, t_f \leq 6 ns, t_f \leq 6 ns. B. C_L includes probe and jig capacitance.

Figure 4. MC3452 and MC3552 Test Circuit and Voltage Waveforms



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