TL-SCSI285, TL-SCSI285Y FIXED-VOLTAGE REGULATORS FOR SCSI ACTIVE TERMINATION SLVS065E – NOVEMBER 1991 – REVISED SEPTEMBER 1998

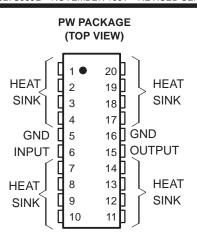
- Fully Matches Parameters for Alternative 2 SCSI Active Termination
- Fixed 2.85-V Output
- ±1% Maximum Output Tolerance at T_J = 25°C
- 0.7-V Maximum Dropout Voltage
- 620-mA Output Current
- ±2% Absolute Output Variation
- Internal Overcurrent Limiting Circuitry
- Internal Thermal-Overload Protection
- Internal Overvoltage Protection

description

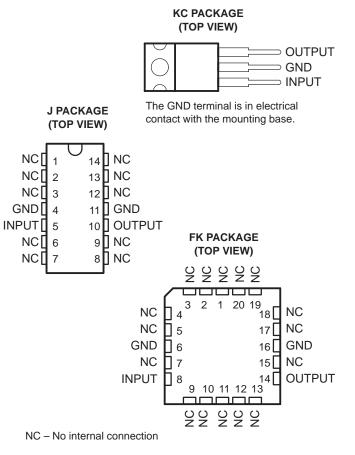
The TL-SCSI285 is a low-dropout (0.7-V) fixed-voltage regulator specifically designed for small computer systems interface (SCSI) alternative 2 active signal termination. The TL-SCSI285 0.7-V maximum dropout ensures compatibility with existing SCSI systems while providing a wide TERMPWR voltage range. At the same time, the \pm 1% initial tolerance on its 2.85-V output voltage ensures a tighter line-driver current tolerance, thereby increasing the system noise margin.

The fixed 2.85-V output voltage of the TL-SCSI285 supports the SCSI alternative 2 termination standard, while reducing system power consumption. The 0.7-V maximum dropout voltage brings increased TERMPWR isolation, making the device ideal for battery-powered systems. The TL-SCSI285, with internal current limiting, overvoltage protection, ESD protection, and thermal protection, offers designers enhanced system protection and reliability.

When configured as a SCSI active terminator, the TL-SCSI285 low-dropout regulator eliminates the 220- Ω and the 330- Ω resistors required for each transmission line with a passive termination scheme, reducing significantly the continuous system power drain. When placed in series with 110- Ω resistors, the device matches the impedance level of the transmission cable and eliminates reflections.



HEAT SINK – These terminals have an internal resistive connection to ground and should be grounded or electrically isolated.





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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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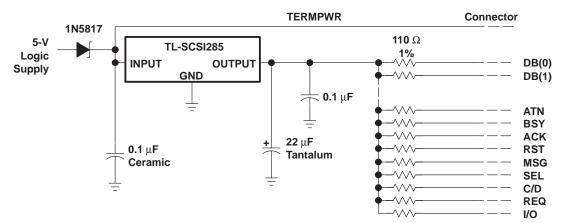
description (continued)

The TL-SCSI285 is characterized for operation from 0°C to 125°C virtual junction temperature. The TL-SCSI285M is characterized for operation from -55°C to 125°C virtual junction temperature.

		AVAILABLE OPTION	IS		
		CHIP			
Тj	CHIP CARRIER (FK)	CERAMIC DIP (J)	PLASTIC POWER (KC)	SURFACE MOUNT (PW) [†]	FORM (Y)
0°C to 125°C	—	—	TL-SCSI285KC	TL-SCSI285PWLE	TL-SCSI285Y
–55°C to 125°C	TL-SCSI285MFK	TL-SCSI285MJ	—	—	11-30312001

[†] The PW package is only available left-end taped and reeled.

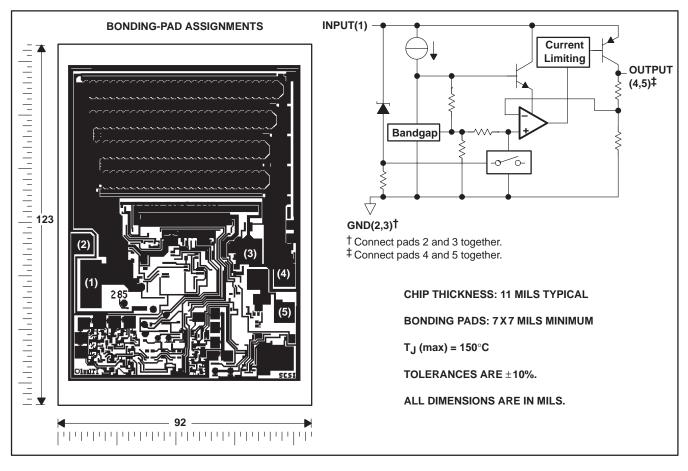
typical application schematic





TL-SCSI285Y chip information

This chip, when properly assembled, has characteristics similar to the TL-SCSI285. Thermal compression or ultrasonic bonding can be used on the doped-aluminum pads. The chips can be mounted with conductive epoxy or a gold-silicon preform.





absolute maximum ratings over operating virtual junction temperature range (unless otherwise noted) $\!\!\!\!^\dagger$

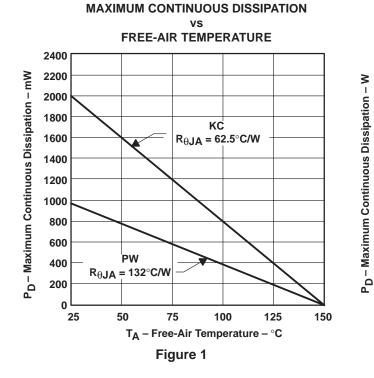
Continuous input voltage, V ₁ 7.5 V
Continuous total dissipation (see Note 1)
Operating virtual junction temperature range, T _A
Storage temperature range, T _{stg} –65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: KC, N, or PW Package
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: J Package

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

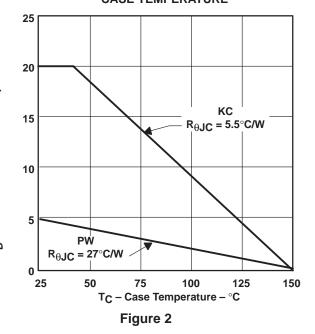
NOTE 1: Refer to Figures 1 and 2 to avoid exceeding the design maximum virtual junction temperature; these ratings should not be exceeded. Due to variation in individual device electrical characteristics and thermal resistance, the built-in thermal overload protection may be activated at power levels slightly above or below the rated dissipation.

DISSIPATION RATING TABLE POWER RATING T ≤ 25°C **DERATING FACTOR** T= 70°C T = 85°C T = 125°C PACKAGE ABOVE T = 25°C POWER RATING POWER RATING POWER RATING AT **POWER RATING** FK ΤA 1375 mW 11.0 mW/°C 880 mW 715 mW 275 mW J TΑ 1375 mW 11.0 mW/°C 880 mW 715 mW 275 mW 2000 mW 16.0 mW/°C 400 mW ΤA 1280 mW 1040 mW KC 20000 mW 182.0 mW/°C‡ 11810 mW 9080 mW 1800 mW TC 7.6 mW/°C Τ_A 950 mW 608 mW 494 mW 190 mW PW 37.0 mW/°C 925 mW 4625 mW 2960 mW 2405 mW TC

[‡] Derate above 40°C



MAXIMUM CONTINUOUS DISSIPATION vs CASE TEMPERATURE





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recommended operating conditions

		TL-S	TL-SCSI285		TL-SCSI285M	
		MIN	MAX	MIN	MAX	UNIT
Input voltage, V _I	T _J = 25°C			3.45	5.5	V
Input voltage, V _I	T _J = full range [†]	3.55	5.5	3.7	5.5	V
Output current, IO	KC package	0	620			
	PW package	0	500			mA
	FK and J packages				480	
Operating virtual junction temperature range, TJ		0	125	-55	125	°C

[†] Full range for the TL-SCSI285 is 0°C to 125°C. Full range for the TL-SCSI285M is –55°C to 125°C.

electrical characteristics, V_I = 4.5 V, I_O = 500 mA, T_J = 25°C (unless otherwise noted)

PARAMETER	TEST CONDITIONS [‡]			TL-SCSI285KC TL-SCSI285N			UNIT
				MIN	TYP	MAX	
Output voltage	I _O = 20 mA to 500 mA,	$V_{I} = 3.55 V \text{ to } 5.5 V,$	$T_J = 25^{\circ}C$	2.82	2.85	2.88	V
Output voltage	$I_{O} = 500 \text{ mA to } 620 \text{ mA},$	$V_{I} = 3.65 V \text{ to } 5.5 V,$	T _J = 0 to 125°C	2.79		2.91	v
Input regulation	$V_{I} = 3.55 \text{ V} \text{ to } 5.5 \text{ V}$				5	15	mV
Ripple rejection	f = 120 Hz,	V _{ripple} = 1 V _{O(PP)}			-62		dB
	I _O = 20 mA to 620 mA				5	30	mV
Output regulation	I _O = 20 mA to 500 mA				5	30	mv
Output noise voltage	f = 10 Hz to 100 kHz				500		μV
Dropout voltage	IO = 500 mA					0.7	V
Diopoul vollage	I _O = 620 mA					0.8	v
	IO = 0				2	5	
Bias current	I _O = 27 mA, equivalent 1 line	e asserted			3	6 mA	
	I _O = 500 mA, equivalent 18	lines asserted (8-bit)			26	49	ША
	I _O = 620 mA				37	62	

+ Pulse-testing techniques are used to maintain the virtual junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.1-µF capacitor across the input and a 22.0-µF tantalum capacitor with equivalent series resistance of 1.5 Ω on the output.



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electrical characteristics, V_I = 4.5 V, I_O = 500 mA, T_J = 25°C (unless otherwise noted)

PARAMETER	TEST CONDITIONS [†]			TL-SCSI285PW			UNIT
PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
Output voltage	$l_{0} = 20 \text{ mA}$ to 500 mA		TJ = 25°C	2.82	2.85	2.88	V
Oulput voltage	$I_{O} = 20 \text{ mA to } 500 \text{ mA},$	$V_{I} = 3.55 V \text{ to } 5.5 V$	T _J = 0 to 125°C	2.79		2.91	v
Input regulation	V _I = 3.55 V to 5.5 V				5	15	mV
Ripple rejection	f = 120 Hz,	V _{ripple} = 1 V _O (PP)			-62		dB
Output regulation	I _O = 20 mA to 500 mA				5	30	mV
Output noise voltage	f = 10 Hz to 100 kHz				500		μV
Dropout voltage	I _O = 500 mA					0.7	V
	IO = 0				2	5	
Bias current	I _O = 27 mA, equivalent 1	line asserted			3	6	mA
	I _O = 500 mA, equivalent	18 lines asserted (8-bit)			26	49	

[†] Pulse-testing techniques are used to maintain the virtual junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.1-µF capacitor across the input and a 22.0-µF tantalum capacitor with equivalent series resistance of 1.5 Ω on the output.

electrical characteristics, V_I = 4.5 V, I_O = 480 mA, T_J = full range (unless otherwise noted)

PARAMETER	TEAT CONDITIONAT		TL-	UNIT			
PARAMETER		TEST CONDITIONS [†]		MIN	TYP	MAX	UNIT
Output voltage	I _O = 20 mA to 480 mA,	$V_{I} = 3.55 V \text{ to } 5.5 V,$	$T_J = 25^{\circ}C$	2.82	2.85	2.88	V
Oulput voltage	I _O = 20 mA to 480 mA,	$V_{I} = 3.7 V \text{ to } 5.5 V,$		2.79		2.91	v
Input regulation	V _I = 3.55 V to 5.5 V,	$T_J = 25^{\circ}C$				15	mV
Ripple rejection	f = 120 Hz,	V _{ripple} = 1 V _{O(PP)}			-62		dB
Output regulation	I _O = 20 mA to 480 mA,	$T_J = 25^{\circ}C$				30	mV
Output noise voltage	f = 10 Hz to 100 kHz				500		μV
Draw and and to an	T.j = 25°C				0.7	V	
Dropout voltage	1] = 25 C					0.85	v
	IO = 0					5	
Bias current	I _O = 24 mA, equivalent 1 line	e asserted				6	mA
	I _O = 480 mA, equivalent 18	lines asserted (8-bit)				49	

† Pulse-testing techniques are used to maintain the virtual junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.1-µF capacitor across the input and a 22.0-µF tantalum capacitor with equivalent series resistance of 1.5 Ω on the output.



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electrical characteristics, VI = 4.5 V, IO = 500 mA, TJ = 25°C

PARAMETER	TEAT AONDITIONAT		TL-SCSI285Y		
PARAMETER	TEST CONDITIONS [†]	MIN	TYP	MAX	UNIT
Output voltage	$I_{O} = 20 \text{ mA to } 500 \text{ mA}, \qquad V_{I} = 3.55 \text{ V to } 5.5 \text{ V}$		2.85		V
Input regulation	VI = 3.55 V to 5.5 V		5		mV
Ripple rejection	f = 120 Hz, V _{ripple} = 1 V _{O(PP)}		-62		dB
	I _O = 20 mA to 620 mA		5		mV
Output regulation	I _O = 20 mA to 500 mA		5		mv
Output noise voltage	f = 10 Hz to 100 kHz	500		μV	
	IO = 0		2		
Bias current	I _O = 27 mA, equivalent 1 line asserted		3		mA
	I _O = 500 mA, equivalent 18 lines asserted (8-bit)		26		mA
	I _O = 620 mA		37		

[†] Pulse-testing techniques are used to maintain the virtual junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.1-µF capacitor across the input and a 22.0-µF tantalum capacitor with equivalent series resistance of 1.5 Ω on the output.



COMPENSATION CAPACITOR SELECTION INFORMATION

The TL-SCSI285 is a low-dropout regulator. This means that the capacitance loading is important to the performance of the regulator because it is a vital part of the control loop. The capacitor value and the equivalent series resistance (ESR) both affect the control loop and must be defined for the load range and the temperature range. Figures 3 and 4 can be used to establish the capacitance value and ESR range for best regulator performance.

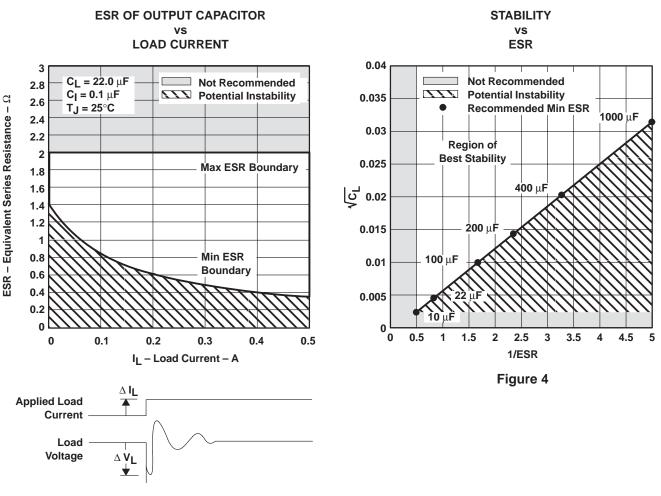


Figure 3



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