SLWS058B - APRIL 1997 - REVISED MARCH 1998

PK PACKAGE (TOP VIEW)

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G

- Wide Operating Frequency Range up to 1000 MHz
- High Output Power: Typical Value of 32.5 dBm at 4.8 V and 900 MHz Typical Value of 29 dBm at 3.6 V and 900 MHz
- High Gain: Typical Value of 9.5 dB at 4.8 V, and 900 MHz at 32.5-dBm Output Power
- High Power-Added Efficiency (PAE): Typical Value of 50% at 32.5-dBm Output Power
- Low Cost
- Extremely Rugged: Sustains 20:1 Load Mismatch
- Suitable for Various Wireless Applications
- Low Leakage <1 μA
- SOT-89 Plastic Power Package

#### description

The TRF7003 power amplifier is a silicon, metal-oxide semiconductor, field-effect transistor (MOSFET) manufactured using the Texas Instruments RFMOS<sup>™</sup> process. It is housed in a SOT-89 (PK) plastic power package. The TRF7003 is intended for global systems for mobile communications (GSM) power amplifier applications. The TRF7003 is a rugged, low-cost device that operates from a single-polarity positive power supply and has low leakage current. Typical power output at 900 MHz is 32.5 dBm, with an associated power gain of 9.5 dB and 50-percent power-added efficiency (PAE).



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.



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## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
NOTE 1: With infinite beatsink and page to flow.

NOTE 1: With infinite heatsink and no air flow

### electrical characteristics over operating free-air temperature range (unless otherwise noted)

#### dc characteristics

	DADAMETED	TEST COND				LIMITS		
PARAMETER		TEST COND	TEST CONDITIONS <sup>‡</sup>		TYP	MAX	UNITS	
ID	Saturated drain current	V <sub>DS</sub> = 4.8 V,	V <sub>GS</sub> = 1.8 V		0.7		А	
9 <sub>m</sub>	Transconductance	V <sub>DS</sub> = 4.8 V,	V <sub>GS</sub> = 1.8 V		1000		mS	
V(TO)	Threshold voltage	V <sub>DS</sub> = 100mV,	I <sub>DS</sub> = 1.5 mA		1.0		V	
V(BR) <sub>sd</sub>	Source-drain breakdown voltage	Ids = 40 $\mu$ A, Source is grounded	$V_{GS} = 0 V$		23		V	
	Leakage current	V <sub>DS</sub> = 4.8 V	$V_{GS} = 0 V$		<1		μA	

 $T_A = 25^{\circ}C$ 

### RF characteristics, $V_{DS}$ = 4.8 V, $V_{GS}$ = 1.8 V

PARAMETER		TEST CONDITIONS§	LIMITS			UNITS
		TEST CONDITIONS <sup>3</sup>	MIN	TYP	MAX	01113
	Output power	Frequency = 900 MHz, P <sub>I</sub> = 23 dBm	31.5	32.5		dBm
	Power gain	Frequency = 900 MHz, Pl = 23 dBm		9.5		dB
η <sub>add</sub>	Power added efficiency	Frequency = 900 MHz, PI = 23 dBm	45	50		%
	Ruggedness test	P <sub>I</sub> = 23 dBm, Frequency = 900 MHz, Load VSWR = 20:1, All phase angles		¶		

 $\frac{1}{2}$  T<sub>A</sub> = 25°C, fixed matching circuit

¶ No degradation in output power after test.



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#### **TYPICAL CHARACTERISTICS**

#### OUTPUT POWER AND POWER ADDED EFFICIENCY

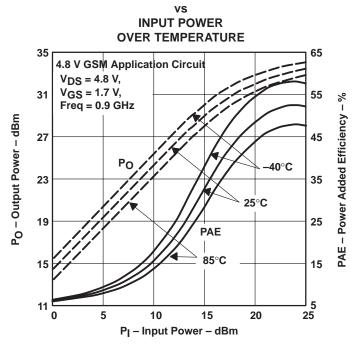


Figure 1

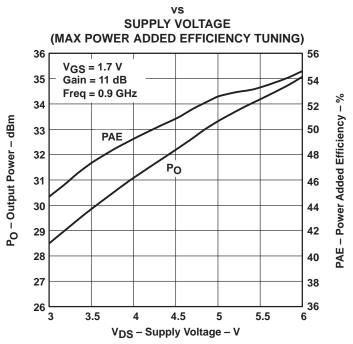
#### OUTPUT POWER AND POWER ADDED EFFICIENCY vs

FREQUENCY **OVER TEMPERATURE** 65 37 PAE –40°C 55 36 % P<sub>O</sub> – Output Power – dBm Power Added Efficiency 25°C 35 45 4.8 V GSM Application Circuit 85°C 35 34 Po –40°Ċ 25 33 25°C I PAE 85°C 32 15 V<sub>DS</sub> = 4.8 V V<sub>GS</sub> = 1.7 V P<sub>I</sub> = 23 dBm 5 31 870 860 880 890 900 910 920 930 f - Frequency - MHz

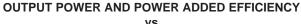




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#### **TYPICAL CHARACTERISTICS**





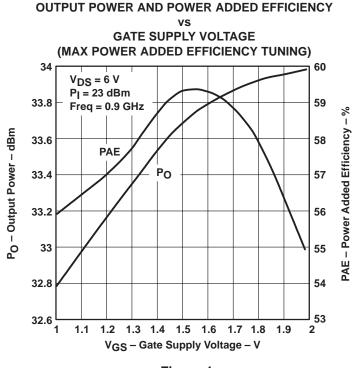
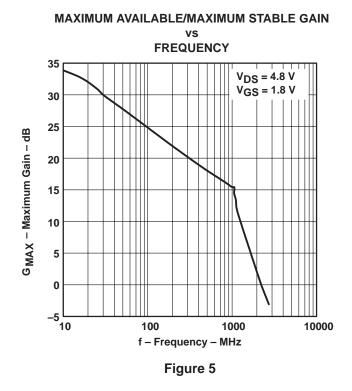


Figure 4



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### **TYPICAL CHARACTERISTICS**



#### Table 1 lists the small signal scattering parameters of the TRF7003.

				0	/ 00		0	
FREQ MHz	S11 (MAG)	S11 (ANG)	S21 (MAG)	S21 (ANG)	S12 (MAG)	S12 (ANG)	S22 (MAG)	S22 (ANG)
100	0.88	-153	9.21	96	0.027	7	0.80	-169
200	0.87	-167	4.61	83	0.027	-6	0.81	-175
300	0.88	-171	3.05	73	0.026	-13	0.82	-176
400	0.88	-174	2.20	65	0.025	-20	0.83	-178
500	0.89	-176	1.76	59	0.024	-25	0.84	-179
600	0.89	-178	1.40	52	0.022	-29	0.84	180
700	0.90	-180	1.15	46	0.022	-33	0.86	178
800	0.90	179	0.99	41	0.021	-38	0.86	177
900	0.91	177	0.84	36	0.019	-40	0.87	176
1000	0.91	175	0.73	31	0.018	-42	0.87	174

Table 1, Small Sig	nal Scattering Paramete	ers, V <sub>DS</sub> = 4.8 V, V <sub>GS</sub> = 1.8 V
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## **TYPICAL CHARACTERISTICS**

Table 2 lists the input and output matching for maximum power-added efficiency (PAE) versus frequency.

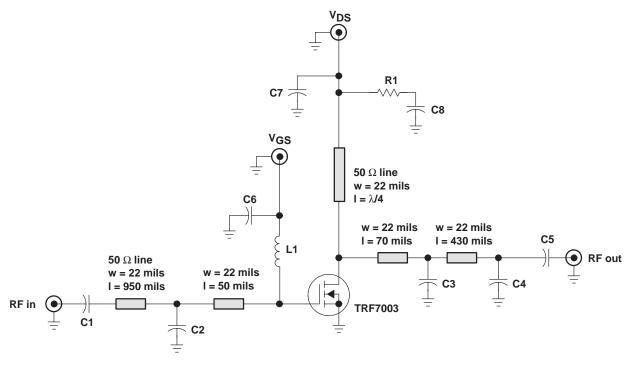
# Table 2. Input/Output Matching for Maximum PAE Versus Frequency, $V_{DS}$ = 4.8 V, $V_{GS}$ = 1.8 V

FREQUENCY MHz	S11 (MAG)	S11 (ANG)	S22 (MAG)	S22 (ANG)
800	.94	165	0.84	164
850	.94	164.5	0.87	163.5
900	.94	164.5	0.88	163
950	.94	164	0.88	162
1000	.94	164	0.88	161



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## **APPLICATION INFORMATION**



Board Material Specifications: Type FR4 ;  $\mathcal{E}_r$  = 4.3 ; h = 12 mils

#### Figure 6. Recommended Application Circuit for 4.8-V GSM

Table 3 lists the TRF7003 components for the recommended 4.8-V GSM application circuit.

#### Table 3. Component List

DESIGNATORS	DESCRIPTION	VALUE	MANUFACTURER	MANUFACTURER P/N
C1	Capacitor	20 pF	ATC™	ATC100A200JP150X
C2	Capacitor	18 pF	ATC	ATC100A180JP150X
C3	Capacitor	16 pF	ATC	ATC100A160JP150X
C4	Capacitor	2.7 pF	ATC	ATC100A2R7CP150X
C5	Capacitor	100 pF	ATC	ATC100A101JP150X
C6	Capacitor	1 μF	MURATA	GRM220Y5V105Z010
C7	Capacitor	100 pF	ATC	ATC100A101JP150X
C8	Capacitor	1 μF	MURATA	GRM220Y5V105Z010
R1	Resistor	30 Ω	International Manufacturing Services	RCI-0402-30ROJ
L1	Inductor	15 nH	ТОКО	LL2012–F15NK

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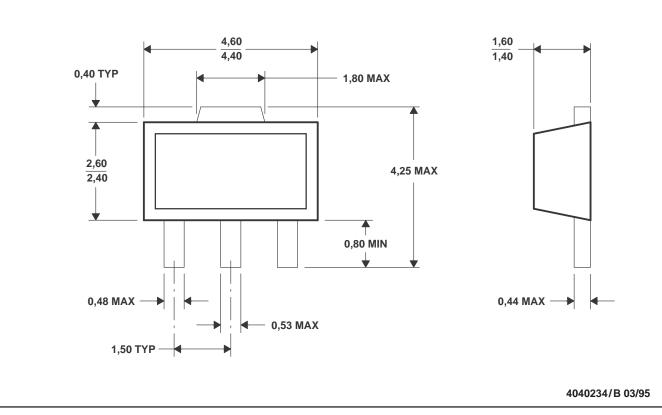


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**MECHANICAL DATA** 

## PK (R-PSSO-F3)

#### PLASTIC SINGLE-IN-LINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
  - C. The center lead is in electrical contact with the tab.



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