# Evaluation Board Documentation TRF8011 and TRF7003 RF Power Amplifier 3.6 Volt Application

APPLICATION BRIEF: SWRA015

Wireless Communication Business Unit

Digital Signal Processing Solutions May 1998



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# TRF8011 and TRF7003 RF Power Amplifier 3.6 Volt Application

#### Abstract

The evaluation board documentation for the Texas Instruments (TI<sup>™</sup>) TRF8011 RF Transmit Driver and TRF7003 RF power amplifier is primarily for device assessment. This document includes the following:

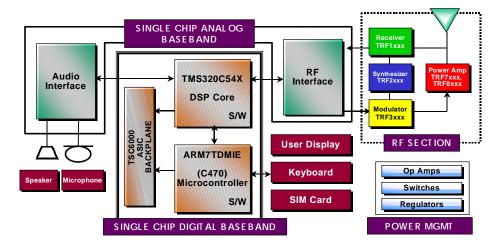
- □ Functional block diagram with terminal functions
- Evaluation board mechanical outline
- Evaluation board schematic for GSM

The schematic includes a component list describing the resistors, inductors, and capacitors, along with the suppliers and board material specifications.

- Typical RF performance
- Test bench diagram with organized instructions for configuration

## **Product Support**

# The TI Advantage Extends Beyond RF to Every Other Major Wireless System Block



#### **Digital Baseband**

TI's single-chip Digital Baseband Platform, combines two high-performance core processors – a digital signal processor tailored for digital wireless applications and a microcontroller designed specifically for low-power embedded systems. The customizable platform helps wireless digital telephone manufacturers lower component counts, save board space, reduce power consumption, introduce new features, save development costs and achieve faster time to market, at the same time giving them flexibility and performance to support any standard worldwide.

#### Analog Baseband

TI analog baseband components provide a Mixed-signal bridge between the real world of analog signals and digital signal processors, the key enabling technology of the digital wireless industry. Using a seamless architecture for wireless communications technology, TI matches its baseband interfaces, radio frequency ICs and power management ICs to digital signal processing engines to create complete DSP Solutions for digital wireless systems.

#### **Power Management**

TI provides power management solutions with integration levels designed to meet the needs of a range of wireless applications. From discrete LDOs and voltage supervisors to complete power supplies for the baseband section, TI power management solutions play an important role in increasing wireless battery life, time-to-market and system functionality.

## For more information visit the Wireless Communications web site at www.ti.com/sc/docs/wireless/home.htm.



The following list specifies product names, part numbers, and literature numbers of corresponding TI documentation.

- □ Data sheet, *TRF8011 900-MHz RF Transmit Driver*, Literature number SLWS056B
- □ TRF8011 Production Bulletin, Literature number SLWT005
- □ *TRF8011 RF Transmit Driver 4.8 Volt GSM Application*, Literature number SWRA008
- □ *TRF7003 RF Power Amplifier 4.8 Volt GSM Application*, Literature number SWRA009

#### World Wide Web

Our World Wide Web site at **www.ti.com** contains the most up to date product information, revisions, and additions. Users registering with TI&ME can build custom information pages and receive new product updates automatically via email.

#### Email

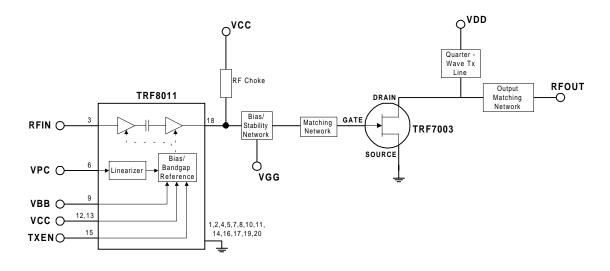
For technical issues or clarification on switching products, please send a detailed email to **sc-infomaster@ti.com**. Questions receive prompt attention and are usually answered within one business day.



## **Functional Block Diagram**

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Figure 1. Functional Block Diagram

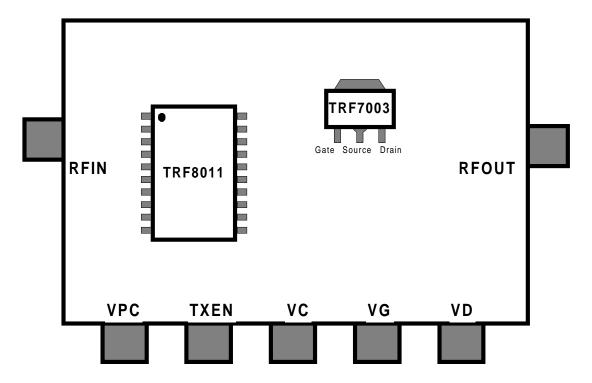


## **Terminal Functions**

| CONNECTOR | I/O | DESCRIPTION  |  |
|-----------|-----|--|--|
| RFIN      | 1   | RF Input. Accepts signal from 800MHz to 1000MHz with |  |
|           |     | 5 dBm maximum input power.                           |  |
| RFOUT     | 0   | RF Output.   |  |
| VPC       | 1   | DC Input for power control signal.                   |  |
| VBB       | 1   | Second stage base bias connection to power supply.   |  |
| VCC       | 1   | First stage bias connection to power supply.         |  |
| TXEN      | 1   | Input for transmit enable logic signal.              |  |
| VDD       | I   | Drain Voltage Input.                                 |  |
| VGG       | 1   | Gate Voltage input.                                  |  |

## **Evaluation Board Mechanical Outline (Top View)**

Figure 2. Evaluation Board Mechanical Outline (Top View)

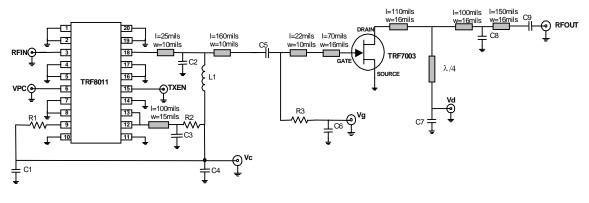


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## **Evaluation Board Schematic**

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Figure 3. Evaluation Board Schematic



| Component List<br>Resistor (Ohms)   | Inductor (nH)   | Capacitor (pF)                    |
|---|---|-----------------------------------|
| R1 = 47<br>R2 = 10<br>R3 = 1K   | L1 = 15   | C1 = 1000<br>C2 = 10<br>C3 = 1000 |
| Supplier:<br>• IMS<br>RCI-0603 series (resistors)<br>• Toko<br>• 61608 series or 11 2012 (i | C4 = 1u<br>C5 = 22<br>C6 = 1000<br>C7 = 1u<br>C8 = 1000<br>C0 = 100 |                                   |
| L61608 series or LL2012 (i<br>• <u>Murata</u><br>GRM36 series (capacitors)                  |   | C9 = 100                          |

• <u>THERMALLY</u> 6390B (heat sink) **Not Needed** 

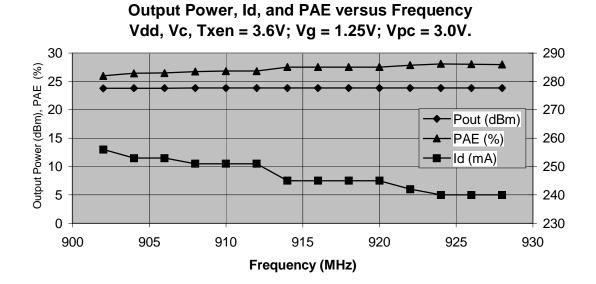
## **Board Material Specifications:**

Type FR4;  $\varepsilon_r = 4.3$ ; h = 12 mils

## **Typical RF Performance**

#### **RF Performance at 0 dBm Input Power**

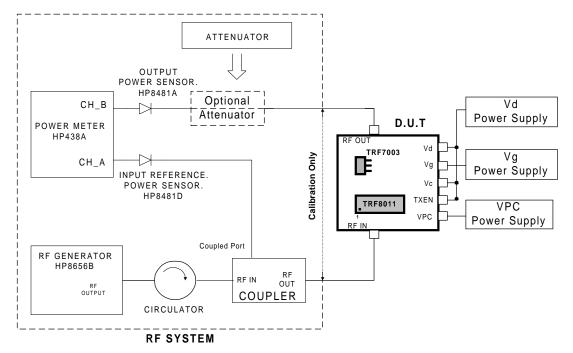
Figure 4. Typical RF performance - Pout/Id/PAE vs. Frequency



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### **Test Bench Diagram**

Figure 5. Typical Bench Test Setup



- 1) Configure Test Bench as shown.
- 2) Calibrate RF SYSTEM less Device Under Test.
  - a) Zero and calibrate POWER METER, POWER SENSORS.
  - b) Set RF power and frequency on RF GENERATOR.
  - c) RF Input Calibration:

Offset *Channel A* of *POWER METER* by the difference between the reading of *Channel B* and *Channel A*. Verify that *Channel A* reading and *Channel B* reading are the same after offsetting.

d) RF Output Calibration:

Attach an Attenuator to *OUTPUT POWER SENSOR*. Offset *Channel B* of *POWER METER* by the difference between the reading of *Channel A* and *Channel B*. Verify *Channel A* reading and *Channel B* reading are the same after offsetting.

- 3) Device D.C. Power-up
  - a) Set *Vd / Vc /TXEN Power Supply* to 3.6 Vdc at the Drain terminal of TRF7003.



- b) Set *Vg Power Supply* to 1.25 Vdc at the Gate terminal of TRF7003.
- c) Adjust *Vg Power Supply* so that Drain Current is about 0.1Amps (without RF).
- d) Set VPC Power Supply to 3.0Vdc for the Power Test

#### NOTE:

Ensure that Vd is always 3.6 Vdc during RF test. Adjustment of *Vd Power Supply* may be required.

## **Evaluation Board Disclaimer**

Please note that the enclosed evaluation boards are experimental Printed Circuit Boards and are therefore only intended for device evaluation.

We would like to draw your attention to the fact that these boards have been processed through one or more of Texas Instruments' external subcontractors which have not been production qualified.

Device parameters measured, using these boards, are not representative of any final data sheet or of a final production version. Texas Instruments does not represent or guarantee that a final version will be made available after device evaluation.

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