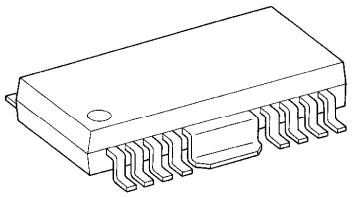
TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT MULTI CHIP

TD62M8500F

8CH LOW SATURATION VOLTAGE SINK DRIVER

The TD62M8500F is Multi Chip IC incorporates 8 low saturation discrete transistors equipped Fly-wheeling Diode and Bias resistor.
This IC is suitable for a battery use motor drive and LED display module applications.



HSOP16-P-300-1.00

Weight : 0.50g (Typ.)

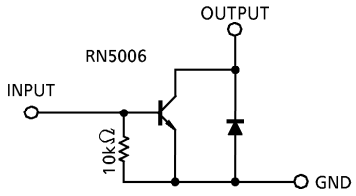
FEATURES

- Suitable for Motor drive circuit and LED display module
- Bias Resistor and Diodes are equipped : R = 10kΩ
- Low Saturation Voltage

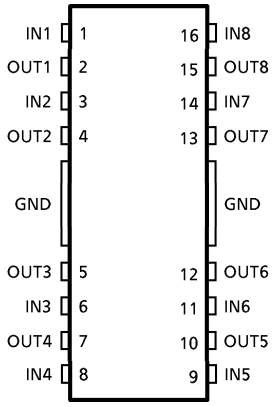
  $V_{CE\ (sat)}$ = 0.16V (Typ.) at I_C = 1A
  $V_{CE\ (sat)}$ = 0.30V (Typ.) at I_C = 2A

- HSOP16 pitch power small package sealed

SCHEMATIC



PIN CONNECTION (TOP VIEW)



961001EBA2

MAXIMUM RATINGS (Ta = 25°C)

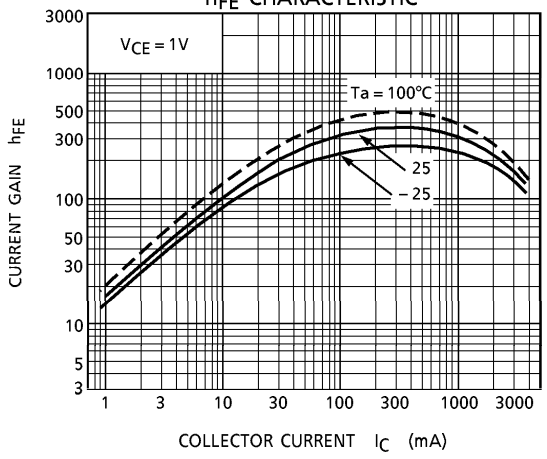
CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V_{CC}	10	V
Breakdown Voltage	V_{CBO}	10	V
	V_{CER}	10	
	V_{EBO}	6	
Output Current	$I_{O\ (AVE)}$	2	A
	$I_{O\ (PEAK)}$	(Note 1) 4	
Base Current	$I_{B\ (AVE)}$	0.4	A
	$I_{B\ (PEAK)}$	0.8	
Fly-wheeling Diode Forward Current	I_F	(Note 2) 2	A
Power Dissipation	P_D	900	mW
Junction Temperature	T_j	150	°C
Operating Temperature	T_{opr}	−40~85	°C
Storage Temperature	T_{stg}	−55~150	°C

(Note 1) T = 10ms MAX. and maximum duty is less than 30%.
(Note 2) T = 10ms single pulse

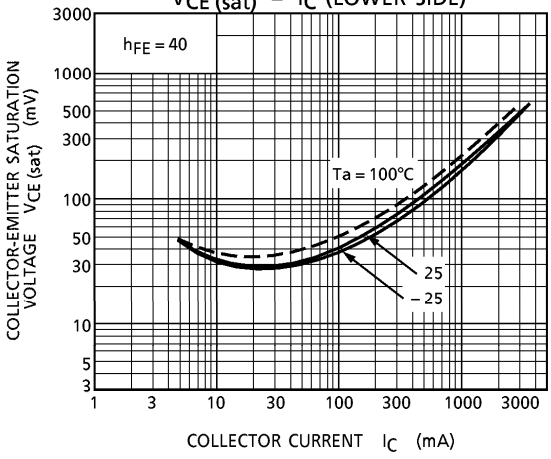
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Current Gain	$h_{FE\ (1)}$	—	V_{CE} = 1V, I_C = 0.5A	160	—	600	—
	$h_{FE\ (2)}$	—	V_{CE} = 1V, I_C = 1.5A	60	130	—	
Saturation Voltage	$V_{CE\ (sat)}$	—	I_C = 1A, I_B = 25mA	—	0.16	0.32	V
			I_C = 2A, I_B = 50mA	—	0.30	0.50	
Transition Frequency	f_T	—	V_{CE} = 2V, I_C = 0.5A	—	150	—	MHz
Leakage Current	I_{OL}	—	V_{CC} = 10V	—	0	10	μA
Fly-wheeling Diode Forward Voltage	V_F	—	I_F = 300mA	—	0.18	1.5	V
			I_F = 450mA, 10ms	—	1.90	—	
Base-Emitter Resistor	R_{BE}	—	—	7	10	13	kΩ
Base-Emitter Forward Voltage	V_{BE}	—	V_{CE} = 1V, I_C = 2.0A	—	0.84	1.5	V

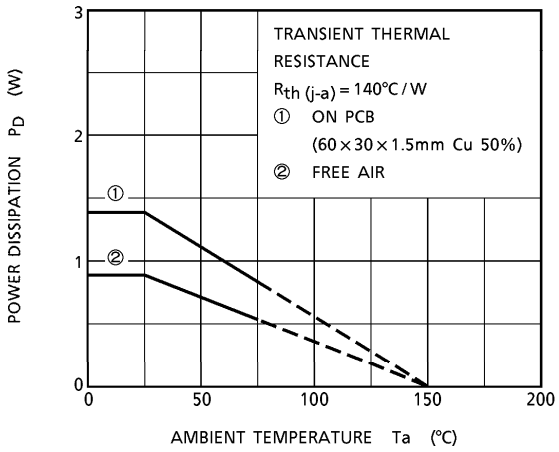
TRANSISTOR (RN5006)
h_{FE} CHARACTERISTIC



TRANSISTOR (RN5006)
$V_{CE\ (sat)}$ – I_C (LOWER SIDE)



P_D – Ta



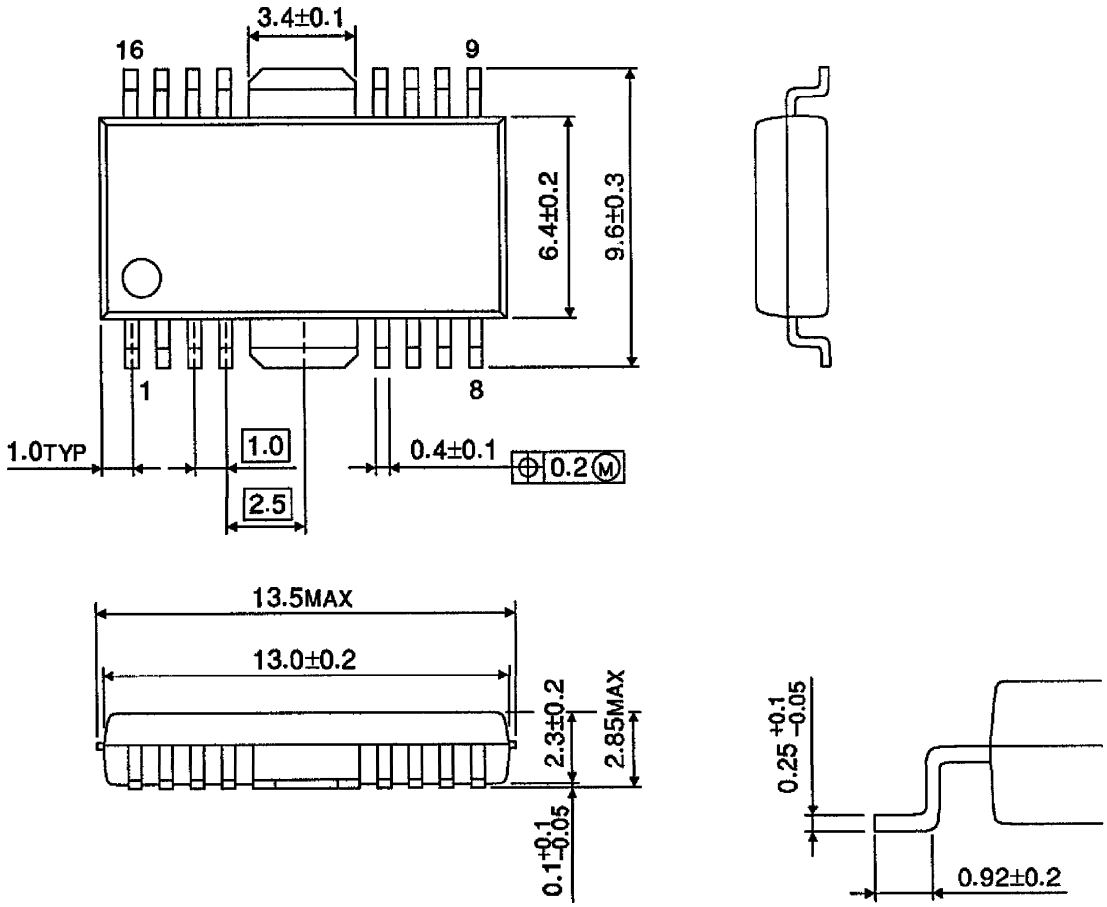
PRECAUTIONS for USING

Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

OUTLINE DRAWING

HSOP16-P-300-1.00

Unit : mm



Weight : 0.50g (Typ.)