TEN TEC CORSAIR I – HISS REDUCTION MODIFICATION (CH1 -28 Feb 14)

What I did at AF board (80984) was:

0. RX on 28.970 spurious signal. Take note of S METER reading.

1. change C10 from 1u to 47n (cut the side of R10 towards C10 and solder the new capacitor between R9 and the cutted side of R10)

2. add a 6u8 capacitor in parallel with R35

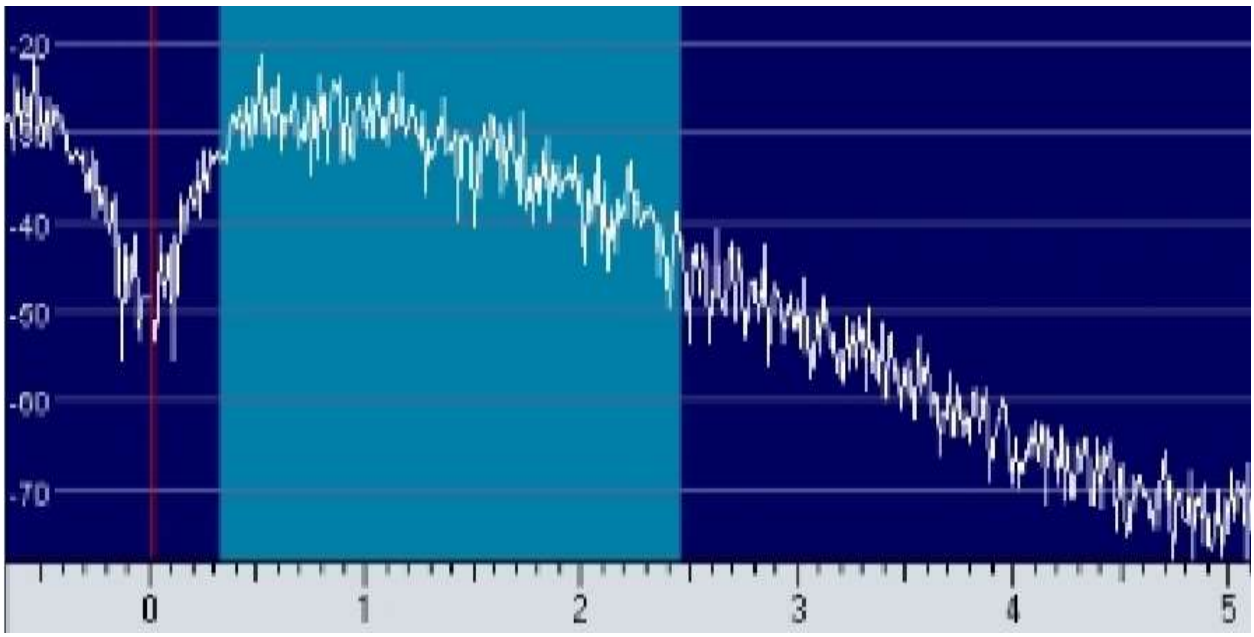
3. add a 47n capacitor between C37 and R78 (cut one side R78 and insert the capacitor in the middle

4. add a 10n capacitor in parallel with R79

5. Add a 30n capacitor in parallel with C9

6. RX again on 28.970 spurious signal, with same conditions as on step 0. Trim R1 to restore previous S METER reading (should be increased about 1 S point).

Modification modify the overall response of the AF chain as shown below.



(Test conditions: Signal from loudspeaker, no antenna connected, just internal white noise, RF GAIN to MAX, AF GAIN to 4, SSB filter @2.4 KHZ, PBT and NOTCH out)

The mod represents a good compromise to allow good SSB and CW reception, minimizing the hiss. Notch filter may contribute to keep the high pitch hiss acceptable, especially in CW mode. Please note you do not need an HI-FI response here! **Important addiction**: Audio strive can be significantly improved by filtering the +13V feeding IF chain @ IF/AF board (40984). Add a 220uF/16V capacitor in parallel with C4.

20/2/2013 - 73, IV3DLW, Giuliano