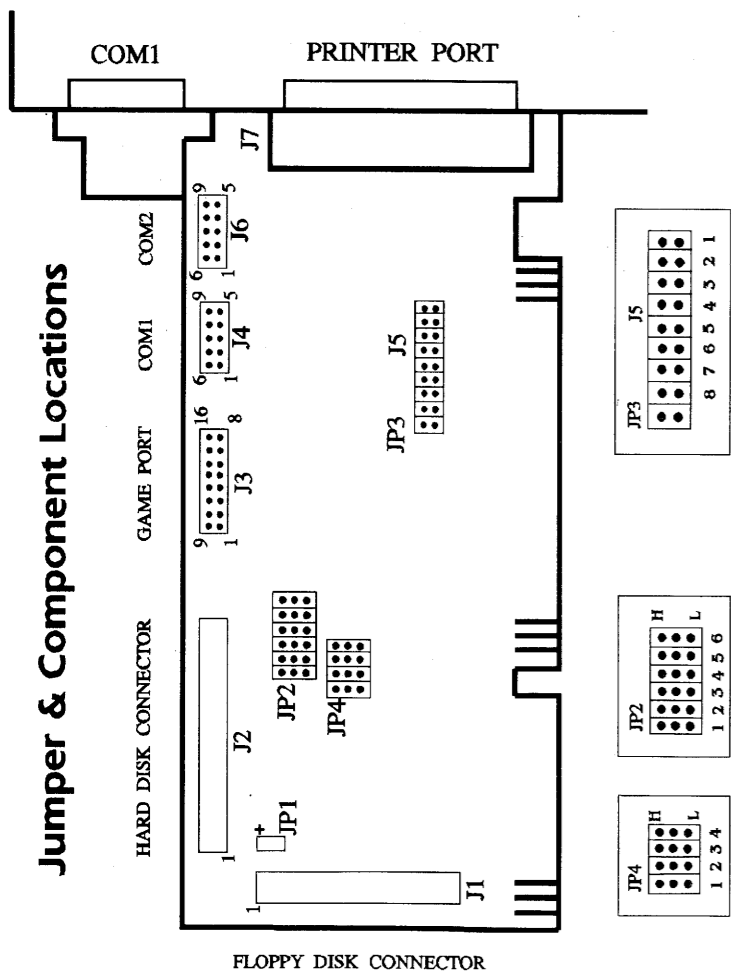


Jumper & Component Locations



JUMPER DESCRIPTION:

JP1 : HDD LED CONNECTOR

JP2 : (1-2) SERIAL PORT 1
 COM4 (2E8H) 1=L 2=L #COM1 (3F8H) 1=L 2=H
 COM3 (3E8H) 1=H 2=L DISABLE 1=H 2=H
 (3-4) SERIAL PROT 2
 COM3 (3E8H) 3=L 4=L #COM2 (2F8H) 3=L 4=H
 COM4 (2E8H) 3=H 4=L DISABLE 3=H 4=H
 (5-6) PRINTER PORT
 LPT3 (3BCH) 5=L 6=L LPT2 (278H) 5=L 6=H
 #LPT1 (378H) 5=H 6=L DISABLE 5=H 6=H

JP3 : PRINTER PORT FUNCTION
 #OUTPUT CLOSE INPUT OPEN

JP4 : (1-2) HARD DISK SELECTION
 #3F6-3F7, 1F0-1F7 1=H 376-377, 170-177 1=L
 #ENABLE 2=L DISABLE 2=H
 (3-4) FLOPPY DISK SELECTION
 #3F0-3F7 3=H 370-377 3=L
 #ENABLE 4=L DISABLE 4=H

J5 : (1-3) SERIAL PORT 1
 #IRQ 4 1=CLOSE OTHER=OPEN
 IRQ 3 2=CLOSE OTHER=OPEN
 IRQ 5 3=CLOSE OTHER=OPEN
 (4-6) SERIAL PORT 2
 #IRQ 3 4=CLOSE OTHER=OPEN
 IRQ 4 5=CLOSE OTHER=OPEN
 IRQ 5 6=CLOSE OTHER=OPEN
 (7-8) PRINTER PORT
 #IRQ 7 7=CLOSE 8=OPEN
 IRQ 5 8=CLOSE 7=OPEN

FACTORY PRESET PIN

Super IDE Controller Card

<< Model : DLW-200 >>

FEATURES

- ★ Support 2 IDE type Hard Disk.
- ★ Support 2 Floppy Disk Drive (1.44M, 1.2M, 720K, 360K).
- ★ Fully programmable serial-interface characteristics:
 - 5-, 6-, 7- or 8-bit character
 - even, odd or no parity bit generation and detection
 - 1-, 1.5- or 2-stop-bit generation.
- ★ Programmable band rate generator allowing division of any input clock by 1 to $(2^{16} - 1)$ and generating the internal 16X clock.
- ★ Compatible with IBM printer port.
- ★ Game port.

Super IDE Controller card

DLW 200

User Manual

DL-WIB200/V2.0