DM7130 Magnitude Comparators

General Description

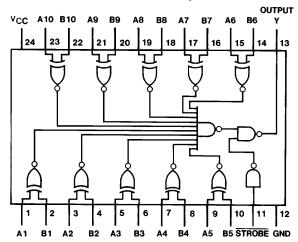
This device offers comparisons to determine equality between two binary words. The DM7130 compares two ten-bit words. A strobe override is provided. When the strobe is taken to a high logic level, the output is forced to a high logic level. The device also features open collector outputs for expansion.

Features

- Typical propagation delay 21 ns
- Typical power dissipation 240 mW
- Open-collector outputs for expansion

Connection Diagram

Dual-In-Line Package



Order Number DM7130J See NS Package Number J24A

Function Table

Condition	STROBE S	Output Y	
$A = B, A \neq B$	Н	Н	
A = B	L	Н	
$A \neq B$	L	L	

 $\begin{array}{ll} H \,=\, \text{High Logic Level} \\ L \,=\, \text{Low Logic Level} \end{array}$

TL/F/6575-1

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 5.5V
Operating Free Air Temperature Range
DM71 -55°C to +125°C

Storage Temperature Range

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM7130			Units
		Min	Nom	Max	Oilles
V _{CC}	Supply Voltage	4.5	5	5.5	V
V_{IH}	High Level Input Voltage	2			V
V_{IL}	Low Level Input Voltage			0.8	V
V _{OH}	High Level Output Voltage			5.5	V
l _{OL}	Low Level Output Current			16	mA
T _A	Free Air Operating Temperature	-55		125	°C

-65°C to +150°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

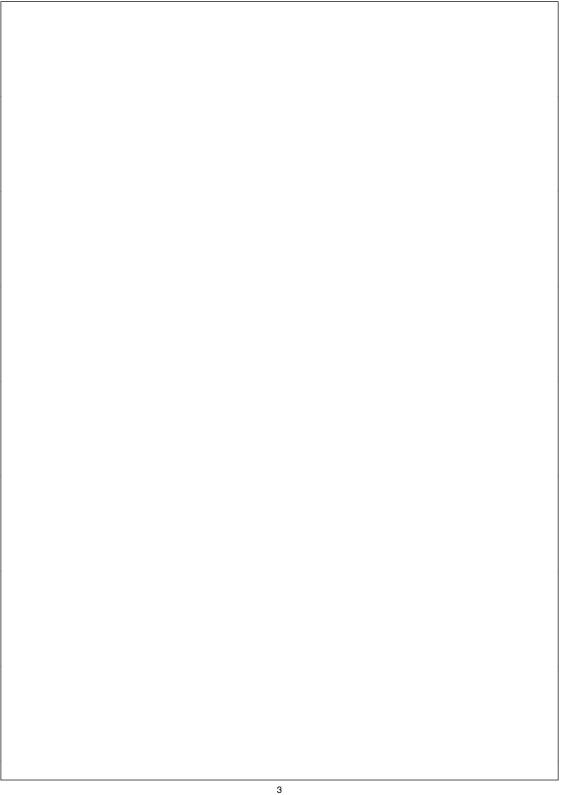
Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -12 \text{ mA}$			-1.5	V
ICEX	High Level Output Current	$V_{CC} = Min, V_O = 5.5V$ $V_{IH} = Min$			100	μΑ
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IL} = Max$		0.2	0.4	V
I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$			1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.4V$			40	μΑ
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-1.6	mA
I _{CC}	Supply Current	V _{CC} = Max (Note 2)		48	70	mA

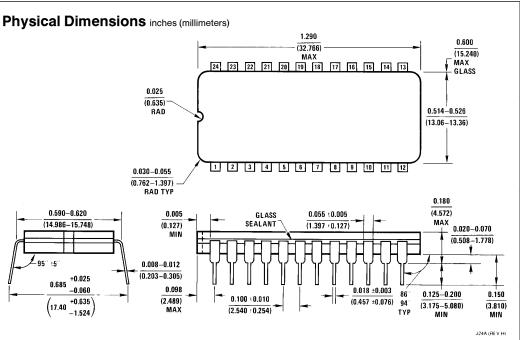
$\textbf{Switching Characteristics} \text{ at } V_{CC} = 5V \text{ and } T_A = 25^{\circ}C \text{ (See Section 1 for Test Waveforms and Output Load)}$

Symbol	Parameter	From (Input) To (Output)	$R_L = 400\Omega$,	Units	
			Min	Max	Onits
t _{PLH}	Propagation Delay Time Low to High Level Output	Data to Output		25	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	Data to Output		40	ns
t _{PLH}	Propagation Delay Time Low to High Level Output	Strobe to Output		18	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	Strobe to Output		30	ns

Note 1: All typicals are at $V_{CC}=5V$, $T_A=25^{\circ}C$.

Note 2: I_{CC} is measured with all inputs grounded and all outputs open.





24-Lead Ceramic Dual-In-Line Package (J) Order Number DM7130J NS Package Number J24A

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