A Fully Differential Input Voltage Amplifier

INTRODUCTION

The instrumentation amplifier is useful for amplifying small differential signals which may be riding on high common mode voltage levels. These amplifiers are particularly useful in amplifying signals in the milli-volt range which are supplied from a high impedance source (> 2 k Ω).

This brief will demonstrate how a low cost, high performance instrumentation amplifier can be built using the newly introduced LM3900 quad amplifier. It is also indicated how a compact transducer bridge amplifier system can be developed to take advantage of the versatility of the LM3900.

BASIC AMPLIFIER OPERATION

Figure 1 shows the basic operation of the amplifier. The bias of the LM3900 is set by the resistors R2 and R3 (neglecting for now, the transistors Q1 and Q2). Current which enters the non-inverting input of the LM3900 will be "mirrored" about V- and then will be drawn into the inverting input terminal. This causes the current to flow through the feedback resistor, R₃, which establishes the output voltage level. If $R_2 = R_3$ and further, if R_2 is connected to ground (0V), then the output voltage biasing level will also be exactly zero volts. It should be noticed that an OUTPUT OFFSET CONTROL can be implemented by supplying a reference voltage, E_R, between R₂ and ground.





Adding transistors Q1 and Q2, as shown in Figure 1 will not disturb this biasing if the two collector currents of the transistors are well matched for a 0V differential input signal. The current sources which bias Q_1 and Q_2 , are chosen to be 100 μ A each to guarantee high β and low offset voltage in Q1 and Q2.

The gain of the amplifier is calculated as follows:

Any differential input voltage, ΔV_{IN} , appears across R₁, and produces a current change ΔI , which is given by:

$$\Delta I = \frac{\Delta V_{IN}}{R_1}$$

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This current change will show up in the collectors of Q1 and Q2 with opposite polarity. The input mirror of the LM3900 returns ΔI_{Q1} to the inverting input terminal where it is added (with sign) to ΔI_{Q2} yielding a total current change of 2 ΔI . This current flows through the feedback resistor, R₃, which causes an output voltage change, ΔVo , which is given by:

$$\Delta Vo = 2\Delta I \times R_3 = 2 \times \frac{\Delta V_{IN}}{R_1} \times R_3$$
 (2)

to yield a gain,

$$= 2 \frac{R_3}{R_1}$$
 (3)

At this point it is convenient to evaluate the result obtained. The gain can be established by one resistor (R1) according to equation (3). Conventional instrumentation amplifiers usually have a gain given by:

 $A_v =$

$$= 1 + \frac{\text{Constant}}{\text{R}}$$
 (4)

This means that the minimum gain of unity is obtained if R is left out (R = ∞). Note that this is different from the result indicated in equation (3) where unity gain is obtained for (5)

 $R_1 = 2R_3$

and minimum gain (or maximum attenuation) is obtained if R_1 is left out ($R_1 = \infty$). This suggests that the amplifier can be turned OFF without disturbing the output voltage dc bias. The two current sources for Q_1 and Q_2 are implemented with a dual transistor (Q_3 and Q_4) in conjunction with an additional amplifier of the LM3900 as shown in Figure 2. The operation can be easily understood if R₄ and R₅ are incorporated within the amplifier, which then takes the form of a conventional opamp closed loop regulator which maintains a reference voltage (the drop across R_6) at the emitter of Q₄.

PERFORMANCE

The performance of the complete instrumentation amplifier of Figure 2 is outlined below (Table I and Figure 3).

TABLE I. Typical Performance Characteristics

GAIN

(1)

 $-34 \text{ dB} (R_1 = \infty) \text{ to } 72 \text{ dB} (R_1 = 0)$ Range of gain $A_v = \frac{2R_3}{2}$ Gain is set according to: R₁ INPUT Voltage offset referred to Pos supply less 2.4V input is adjustable to zero. Common-mode and Neg supply less 300 mV differential input voltage Common-mode rejection 115dB (gain of 1000) ratio at 10 Hz Bias current (either input) 200 nA OUTPUT Output offset is 12 mV_{rms} (open loop) adjustable to zero. Output noise $3 \text{ mV}_{\text{rms}}$ (A_{CL} = 66 dB) FREQUENCY RESPONSE 1 MHz (gain of 1000) Small signal frequency response (-3 dB) 3 MHz (gain of 1)

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FIGURE 2. Bridge Amplifier

Since quantitative discussion of the sources of offset voltage is beyond the scope of this brief, only the procedure for nulling the amplifier will be included.

Letting R_1 go to zero causes the amplifier to operate in the open-loop mode. The main offset voltage source is now the V_{BE} mismatch of Q₁ and Q₂. The output can be nulled by the OUTPUT OFFSET CONTROL (the reference voltage for R₂) or by adjusting the value of R₂. With R₁ = ∞ , the main offset voltage source is the mismatch in the collector currents of Q₃ and Q₄. This is easily adjusted via R₁₂. These first and second adjustments interact, however, after repeating the procedure a couple of times a good result is obtained

TRANSDUCER BIAS SOURCE

Having in mind that the LM3900 consists of four independent amplifiers makes it relatively easy to bias a transducer bridge with a constant current source using only one more of the amplifiers and one resistor. The technique is self-explanatory and is also shown in Figure 2.

CONCLUSION

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A brief review of a new concept for an instrumentation amplifier has been presented. Many applications can be de-



rived from this basic connection which require amplifying the low level differential signals which are obtained from sensors such as strain gages, pressure transducers, and thermocouples. The performance of this instrumentation amplifier is adequate for many system applications. (See National Semiconductor Application Note 72, "The LM3900 — A New Current-Differencing Quad of ± Input Amplifiers" for further information.)



FIGURE 3. Frequency Response

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