Interfacing the DP8392 to 93 Ω and 75 Ω Cable

National Semiconductor Application Note 620 Mohammed Rajabzadeh September 1992



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The DP8392 Ethernet Coaxial Transceiver Interface (CTI) is designed primarily for 10BASE2 and 10BASE5 applications which use 50Ω coaxial cable. However, with minor modificaance cables. This article shows how to use the DP8392 with 75Ω or 93Ω cable. The trade off is that segment span is reduced to accommodate for higher series DC resistance of these cables. The CTI is a current driver. The two important

DYNAMIC RANGE

ter and collision detection levels.

The dynamic range of the transmitter is important in the following case:

factors that must be handled properly in using the chip with

 75Ω and 93Ω cables are the dynamic range of the transmit-

Suppose two stations collide with one-another. To detect collisions properly, each station must sink at least as much DC current as it would in a non-collision case. This would mean that with the 93 Ω cable when a collision occurs the chips should be able to sustain approximately -4V DC level. If the signals from the colliding stations are in phase the AC signal could be 8V peak to peak.

The DP8392's transmitter clamps before it pulls to -8V. However, when it clamps it also changes the duty cycle enough to sustain the -4V DC collision level.

An internal diode is included in series with the transmitter's output to isolate its capacitance and thereby minimizing the tap capacitance. For more dynamic range margin, it is recommended that external isolating diodes at the transmitter output not be used. It is also advisable to design the power supply to operate at the higher end of the 8.55V to 9.45V range.

COLLISION LEVELS—RECEIVE MODE

In order to understand the concerns with collision levels, it is necessary to calculate the levels for Cheapernet (10BASE2) 50 Ω cable (RG58AU) as an example.

50 Ω Cable Example (RG58A/U)

Table I shows the parameter values that are used in calculating the collision levels. Please note that all the levels in this article are for receive mode collision detection.

TABLE I. Assumptions and Definitions				
R _T	= Termination Resistor at 20°C	= 50 ±1%	802.3	
t _T	= Temp. Coef. of the Terminator	= 0.0001/°C	ASSUMPTION	
L	= Maximum Segment Length	= 185m	802.3	
R _{DC}	= Maximum Cable DC Res. at 20°C	$= 0.0489 \Omega/m$	BELDEN	
tc	= Temp. Coef. of Copper	= 0.004/°C	PHYSICS	
Tm	= Maximum Cable Temp.	= 50°C	ASSUMPTION	
SR	= Step Response at Max Cable Length	= 0.98	NATIONAL	
R _C	= Max Connector Res./Station	= 0.0034Ω	MIL SPEC	
I _{B+}	= Max Positive Bias Current	= 2 μA	802.3	
I _B -	= Max Negative Bias Current	= 25 μA	802.3	
I _{max}	= Max DC Drive Current	= 45 mA	802.3	
I _{min}	= Min. DC Drive Current	= 37 mA	802.3	
R _O	= Non Transmitting Output Impedance	= 100 kΩ	802.3	
Ν	= Max Nodes per Segment	= 30	802.3	
SK	= Skew Factor, Effect of Encoder Skew on DC Level = (SKEW × 4)/100	= 0.02 for 0.5 ns Skew	802.3	
R _S	= Max DC Loop Res. of a Segment		DEFINITION	
RL	= Load Resistance Seen by a Driver		DEFINITION	
SEO	= Sending End Overshoot	= 0.08	ASSUMPTION	

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The collision levels that need to be calculated are V_{max} and $V_{min}.$ The V_{max} or "no detect" level is the maximum DC voltage generated by one node. The worst case here occurs when the transmitting node is at the center of a maximum length cable, and the collision is being detected either by itself or by a station right next to it. On the other hand, the V_{min} or "must detect" level is the minimum DC voltage generated by two minimum stations transmitting at one end of a

maximum length cable, and the collision is being detected by a node on the other side of the cable.

The filter impulse response is not included in these calculations since it is mutually exclusive with the Sending End Overshoot. If the impulse response is larger than the Sending End Overshoot, the exceeding portion should be added on to the limits.



	Collision Level Calculation	ifforant impada	a apple. Here are these peremeters for 000 acht-			
	ameters need to be changed when using a d I TYPE, BELDEN 9269);	mereni impedanc	ce cable. Here are those parameters for 93Ω cable			
(11002) (10		TABLE II				
RT	= termination resistor at 20°C	$= 93 \pm 1\%$				
L	= maximum segment length	= 130m				
- R _{DC}	= maximum cable DC res. at 20°C	$= 0.1437 \Omega/m$	BELDEN			
	g the new values the V _{max} and V _{min} levels are	9:				
_	Maximum Non Collision Level V	_{nax} (No Detect)–				
R _{Tmax}	$= 93 \times 1.01 \times [(50 - 20) \times 0.0001 + 1]$		= 94.212Ω			
R _S	$= 0.1437 \times 130[(50 - 20) \times 0.004 + 1] +$	30×0.0034	$= 21.025\Omega$			
RL V	= (94.212 + 21.025/2)/2 = $[45 \times 1.02 + 29 \times 0.025] \times 52.362 \times 1.$	08	= 52.362Ω = 2636.692 mV			
V _{max}	[45 ~ 1.02 + 28 ~ 0.025] ~ 52.502 ~ 1.	00	2030.032 114			
	Minimum Collision Level V _{min}	(Must Detect)—F	Receive Mode—93 Ω Cable			
R _P	$= [100k/28]/(93 \times 0.99) = 3571/92.07$		$= 89.756\Omega$			
VD	$= 2 \times 37 \times 0.98 \times [92.070//(21.025 + 8)$, -	= 3646.396 mV			
V _{min}	= 3646.396 × [89.756/(21.025 + 89.756))] × 0.98	= 2895.272 mV			
93Ω IMPLI	EMENTATION WITH DP8392	It is also	to necessary to add the resistor R_3 ($R_3 = R_1 / / R_2$) in			
	nows the connection diagram with 93 Ω cable (1		series with the CDS pin. This will assure that the voltage			
	d 30 stations). The design parameters defined b		drop due to the biasing currents into CDS and RXI pins are			
	immarized in Table III. The resistor divider ra		duplicated.			
needs to b	be calculated to attenuate the receiver input s	ig- To che	To check the design;			
nal. The tw	vo resistors R1 and R2 should center the calcul	at- [54.8k/	$[54.8k/(54.8k + 45.2k)] \times 2636 \text{ mV} = 1444 \text{ mV}$			
	lds (2636 mV to 2895 mV) to the internal level	, -	$((54.8k + 45.2k)] \times 2895 \text{ mV} = 1586 \text{ mV}$			
•	450 mV to 1580 mV).		The DP8932's internal collision range is within this window.			
	or divider and the capacitor Cp, <i>Figure 1</i> , (Cp RXI input capacitance, typically 1 pF, and the		75 Ω CABLE IMPLEMENTATION			
trace capa	citance associated with it) form a low pass fil	ter This me	This method can also be successfully implemented for 80			
effect. It m	ay be necessary to add the capacitor Cc (capa	ci- meters	meters of 75 Ω cable (RG59/U BELDEN 8241). The collision			
	ates a high pass effect) to compensate the le		olds are 2127.8 mV and 2339.6 mV. The correspond-			
pass effect	t. The equation to calculate the capacitor Cc is		and R ₂ values are 67.8 k Ω and 32.2 k Ω respectively.			
	$Cc \times R_2 = Cp \times R_1$	I able I	V summarizes the design parameters.			
	TABLE III		TABLE IV			
CABLE	BELDEN RG62A/U Type 93 Ω Cal	ole CABLE				
		L L				
L R _{DC}	130 meters 0.1437 Ω/m	R _{DC}	80 meters 0.1894 Ω/m			
N	30	N	30			
R ₁	54.8k	R ₁	67.8k			
R ₂	45.2k	R ₂	32.2k			
	V _C					
	DP8392	= c _P \$ ^R 1 _{R2}				
	RXI		Coax			
	NA1					
	тхо					
	CDS					
		$R_3 = R_1 / / R_2$	TL/F/10444-3			
		FIGURE 1				

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