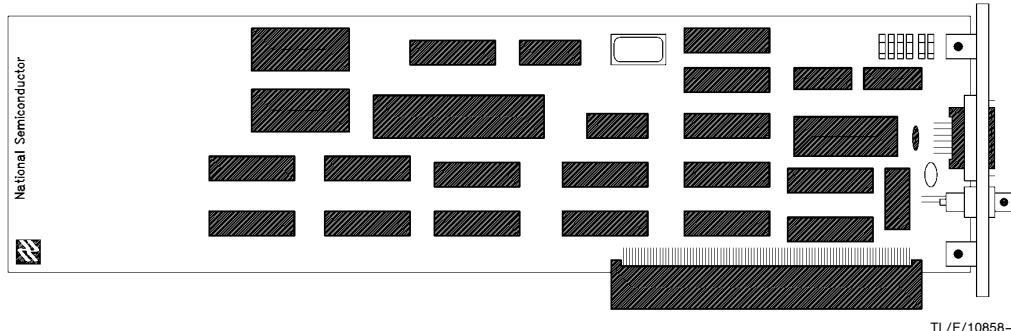


Apple Macintosh Ethernet Add-In Card

National Semiconductor
System Brief 108
August 1990



SYSTEM DESCRIPTION

Local Area Networks have emerged as a widely accepted, practical method for connecting personal computers, printers, and workstations together to form workgroups and corporate networks. Ethernet®, the IEEE 802.3 standard, has the largest installed LAN base because of its low cost and high performance, easy to use characteristics. Ethernet networks have been dominated by IBM® PCs and compatibles. However, Apple Macintosh connectivity to the work group is a key emerging application. An Ethernet adapter board receives and transmits, packets of data through the cable implementing a CSMA/CD (Carrier Sense, Multiple Access, Collision Detect) protocol to ensure the integrity of data transmission and reception. It encodes the data to Manchester code for transmission and decodes to Non Return Zero at reception. The data is processed through the controller by IEEE 802.3 protocols and buffered into local memory. This data must be accessed efficiently in and out of the bus. A software driver must be written to interface the adapter card to the Network Operating System.

For the Apple Macintosh II adapter, the Ethernet controller must have the capability to address 32 bits, interface to the Mac II Nubus, have an efficient memory buffer scheme, implement a low-cost solution, perform a high data throughput, and have a simple interface to the software. The transceiver must conform to IEEE 802.3 standards to interoperate with other Ethernet components on the network.

DESIGN CHALLENGES

1. Physical Layer Conformance and Reliability must meet system design requirements.
2. Maximize data throughput in a shared memory architecture implemented in the design without adding cost.
3. Simple interface to Nubus I/O needs the Ethernet controller to have byte swapping capabilities, a low interface chip count, support a 32-bit bus, and support transfers to registers and requests through arbitration logic.
4. Physical Address ROM to contain the unique address assigned to each Ethernet node.

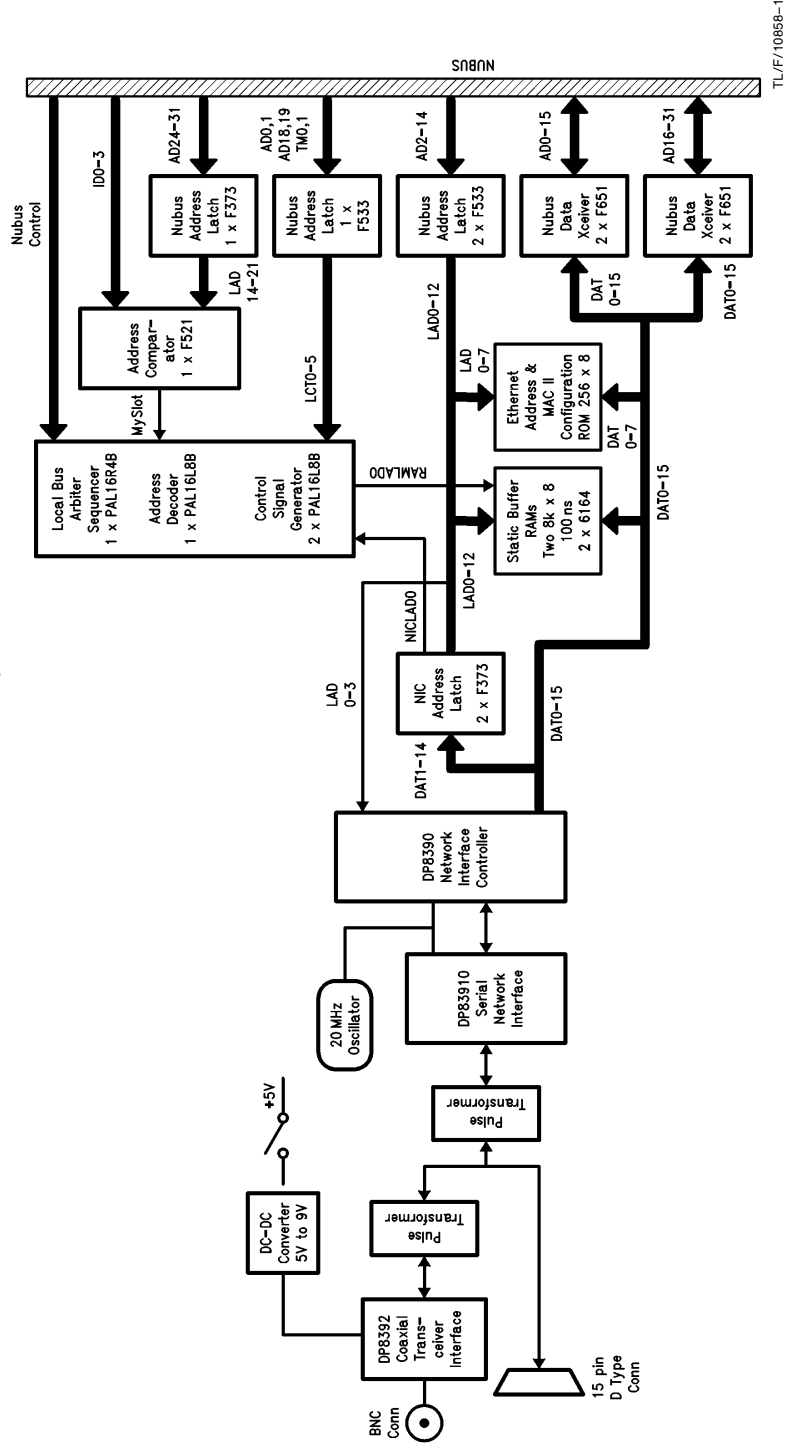
COMPONENTS

1. Coaxial Transceiver Interface, and the Serial Network Interface (Manchester Encoder/Decoder) meet IEEE 802.3 standards. Use minimal external components, have collision detect, squelch, jabber and CD Heartbeat in the transceiver; plus, jitter tolerance, loopback capability, and reliable phase lock loop in the Encoder/Decoder.
2. Network Interface Controller is the Ethernet protocol controller with memory management.
3. PAL 16R4 and 74651 for synchronous sequencer logic, and data transceivers for the 32-bit Nubus transfers.
4. LS471 ROM is the physical address for the card and contains the configuration data and has the ability to also contain the device driver data.
5. 616A SRAM is local buffer memory.

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MAC II PARTS LIST				
Function	Description	NSC Part	Other	Qty
Transceiver	Coax Transceiver Interface	DP8392		1
	DC-DC Converter		✓	1
Network Interface	Serial Network Interface	DP83910		1
	Network Interface Controller	DP8390		1
Address/Data Interface	PAL	PAL16R4B		1
	PAL	PAL16L8B		3
	Address Latch	74373		2
	Address Comparator	74521		1
	Nubus Address Latch	74533		3
	Nubus Address Latch	74373		1
Memory	Nubus Data Transceiver	74651		4
	Static Buffer Ram	6164		2
	256 x 8 ROM	74LS471		1

Macintosh II Ethernet Adapter Card



TL/F/10858-1

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National Semiconductor Corporation
1111 West Bardin Road
Arlington, TX 76017
Tel: 1(800) 272-9959
Fax: 1(800) 737-7018

National Semiconductor Europe
Fax: (+49) 0-180-530 85 86
Email: cnjwge@tevm2.nsc.com
Deutsch Tel: (+49) 0-180-530 85 85
English Tel: (+49) 0-180-532 78 32
Français Tel: (+49) 0-180-532 93 58
Italiano Tel: (+49) 0-180-534 16 80

National Semiconductor Hong Kong Ltd.
19th Floor, Straight Block,
Ocean Centre, 5 Canton Rd.
Tsimshatsui, Kowloon
Hong Kong
Tel: (852) 2737-1600
Fax: (852) 2736-9960

National Semiconductor Japan Ltd.
Tel: 81-043-299-2309
Fax: 81-043-299-2408

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