Ising COP800 Devices to Control DC Stepper Motors

Using COP800 Devices to Control DC Stepper Motors

National Semiconductor Application Note 714 Michelle Giles September 1990

INTRODUCTION

COP800 devices can be used to control DC stepper motors with limited effort. This application note describes the use of a COP820 to control the speed, direction and rotation angle of a stepper motor. In addition to the COP820, this application requires a quad high current peripheral driver (DS3658) to meet the high current needs of the stepper motor.

DC STEPPER MOTOR

A DC stepper motor translates current pulses into rotor movement. A typical motor contains four winding coils labeled red, yellow/white, red/white, and yellow. Applying current to these windings forces the motor to step. For normal operation, two windings are activated (pulsed) concurrently. The motor moves clockwise one step per change in windings activated with the following activation sequence: red and yellow, yellow and red/white, red/white and yellow/ white, yellow/white and red, repeat. Half-steps may be generated by altering the sequence to: red and yellow, yellow, yellow and red/white, red/white, red/white and yellow/ white, yellow/white and red, red, repeat. The motor runs in a counterclockwise direction if either sequence is applied in reverse order. The speed of rotation (number of steps/second) is controlled by the frequency of the pulses.

COP820 CONTROL OF STEPPER MOTOR

The COP820 controls the stepper motor by sending pulse sequences to the motor windings in response to control commands. Commands executed by the code in this application include: single step the motor in a clockwise or counterclockwise direction (i.e. rotate the rotor through a certain number of degrees), run the motor continuously at one of four speeds in a clockwise or counterclockwise direction, and stop the motor.

Note: Half-stepping is not implemented in this example.

During continuous mode operation, the 16-bit timer of the COP820 is used to control the speed of the stepper motor. The timer is set up with a value that causes an underflow once every x seconds or at a frequency of 1/x. Each underflow of the timer interrupts the microcontroller. In response to the timer interrupt, the microcontroller generates a new pulse and causes a single step of the motor. Thus the motor steps at the frequency of the timer underflows. This application sets up the timer to generate interrupts at four different frequencies. These frequencies produce the following motor speeds: 25 steps/second, 100 steps/second, 200 steps/ second, and 400 steps/second.

The determination of which windings to activate and deactivate to step the motor is performed by a single subroutine in this example. A block of memory is allocated to store a step pointer and the four possible stepper drive values are shown in Table I (9,C,6,3). Consecutive memory locations are used to store the stepper drive values so that applying the value from location X and then location X+1 (or X-1) causes the motor to step once. The motor drive subroutine increments or decrements the pointer to the current drive value based on the selection of a clockwise or counterclockwise direction. Writing the value from the newly selected location to the motor causes a single step of the motor in the appropriate direction.

During single step operation, the microcontroller steps the motor the exact number of times requested in the control command. Each step corresponds to 1.8 degrees of rotor movement. Therefore, a request to perform 200 steps will rotate the rotor through one complete revolution (360 degrees) at a fixed speed.

A block diagram of the application is shown in Figure 1. A flowchart of the code used to control the motor is given in Figure 2. The complete code is given at the end.

TABLE I. Stepper Motor Drive Sequence

Step	Yellow	Red/White	Yellow/White	Red	Hex Value
0	ON	OFF	OFF	ON	9
1	ON	ON	OFF	OFF	С
2	OFF	ON	ON	OFF	6
3	OFF	OFF	ON	ON	3
4	ON	OFF	OFF	ON	9

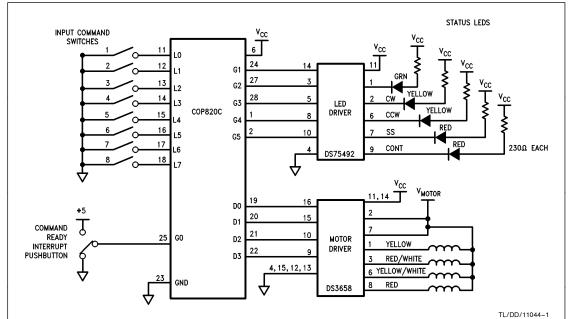
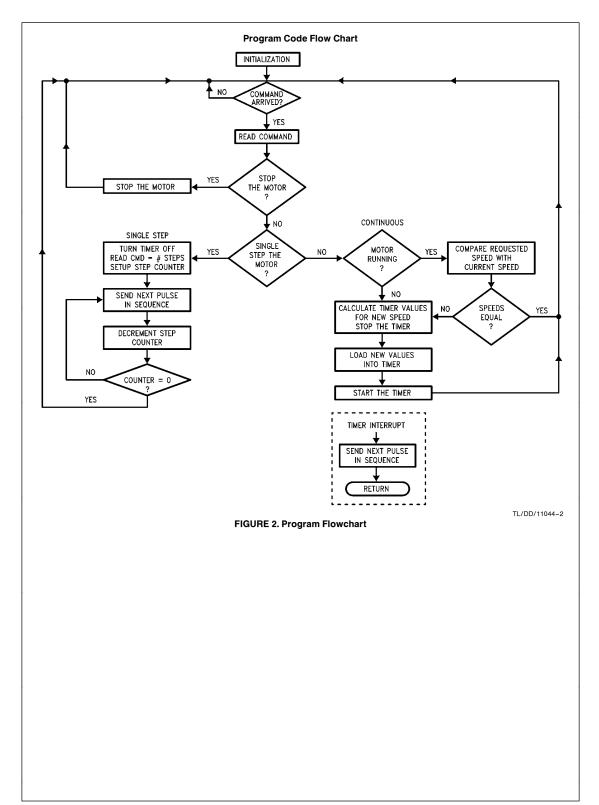


FIGURE 1. Schematic Diagram



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NATIONAL SEMICONDUCTOR CORPORATION
COP800 CROSS ASSEMBLER, REV: D1, 12 OCT 88
                                                           ; STEPPER MOTOR CONTROL PROGRAM
    2
                                                           ;MAY 1990
                                                           This program controls the speed, direction, and degree of rotation of a DC stepper motor.
                                                                                                                                Memory Map
                                                                             Memory Map

CONTENTS

(MS0) step motor drive value 09H (two windings active per pulse)

(MS1) step motor drive value 0CH

(MS2) step motor drive value 08H

(MS3) step motor drive value 08H

(CMD) control command

bit7 - bit4 = motor speed or upper nibble of # single steps

bit 3 = unused

bit 2 = (MODE) single step or continuous mode select (1 = ss)

bit 1 = (DIR) cw or ccw direction select (1 = cw)

bit 0 = (GO) motor go or motor stop select (1 = stop)

(STEPS) lower byte of number of single steps

(FLGREG) flag register

bit 0 = (INT) ready to read in cmd (ext int occured)

bit1 - bit7 = unused

(TVAL0) value to load in lower byte of timer for speed X

(TVAL1) value to load in upper byte of timer for speed X

(PORTLP) port L input pins used for incomming commands

(PORTGD) port G data pins used to drive status LEDs

(PORTD) port D data pins used to ouput pulses to the stepper motor

(CREG0) step counter register zero

(CREG1) step counter register one

(STPPTR) pointer to current step motor drive value (RAM 00 - 03)
                                                               RAM
                                                                              CONTENTS
                                                                  00
  10
                                                                 01
02
 11
12
                                                                  03
 13
                                                                  04
 15
 16
17
 19
                                                                  05
 21
 23
                                                                  14
 24
25
                                                                  D2
                                                                  D4
DC
 27
 29
                                                                  F1
F2
 30
 31
32
                                                           REGISTER AND CONSTANT DEFINITIONS
 33
 34
                                                           ; COMMAND BITS
 35
                     0000
                                                                              GO
                                                                                                  = 0
                                                                                                                                                               GO COMMAND BIT
                                                                                                                                                                                                               0 = GO
 36
                                                                                                                                                                     1 = STOP
                                                                                                                                                               ; DIRECTION COMMAND BIT
; 1 = CW 0 = CCW
; MODE COMMAND BIT
 37
                      0001
                                                                              DIR
                                                                                                   = 1
 38
 39
                      0002
                                                                              MODE
                                                                                                  = 2
                                                                                                                                                               ; 1 = SINGLE STEP 0 = CONTINUOUS
 40
                                                           ; PORTG BITS
 42
                                                                                                                                                              ;FLAG BIT (SET IF EXTINT OCCURS)
;READY LED
;CLOCKWISE LED
  43
                      0000
                                                                                                   = 0
                                                                                                  = 0
= 1
= 2
= 3
  44
                      0001
                                                                              READY
                     0002
0003
  45
                                                                              CW
                                                                                                                                                              ; COUNTER CLOCKWISE LED
; SINGLE STEP LED
  46
  47
                      0004
                                                                               SS
                                                                                                  = 4
                                                                                                                                                               (CONTINUOUS (NON-STOP) LED
  48
                      0005
                                                                              NS
                                                          ; REGISTERS
 50
                      0004
                                                                                                   = 04
                                                                              CMD
                                                                                                                                                              ; INPUT COMMAND STORAGE REGISTER
                                                                                                                                                                                                                              TL/DD/11044-3
```

NIATIO	NIAL OF MOONDU	OTOD CODDODATION					
NATIONAL SEMICONDUCTOR CORPORATION							
COP80	00 CROSS ASSEMI	BLER, REV: D1, 12 OCT 8	8				
			-				
52	0005	STEPS	= 05	; INPUT #STEPS/SPEED REGISTER			
53							
		CREG0	= 0F0	; COUNTER REGISTER			
54	00F1	CREG1	= 0F1	; COUNTER REGISTER			
5.5	0007	FLGREG	= 07	;FLAG REGISTER (FLAG BITS)			
56	00F2	STPPTR	= 0F2	CURRENT MOTOR STEP POINTER			
57	0014	TVALO	= 014	:MOTOR SPEED LOAD VALUES			
58	0015	TVAL1	= 015	•			
59	0000	MSO	= 00	STEPPER MOTOR DRIVE VALUES			
60	0001	MS1	= 01	, STEET EN MOTOR BRITE TABLES			
61	0002	MS2	= 02				
62	0003	MS3	= 03				
	0003	#33	- 03				
63							
64		; ASSIGNMENTS FO	R COP820				
65							
66	00D5	PORTGC	= 0D5				
67	00D4	PORTGD	= 0D4				
68	00D6	PORTGP	= 0D6				
69	00D1	PORTLC	= 0D1				
70	00D0	PORTLD	= 0D0				
71	00D0	PORTLE	= 0D2				
72	OODC	PORTD	= ODC				
73	00D7	PORTI	= 0D7				
74	00E9	SIOR	= 0E9				
75	OOEA	TMRLO	= OEA				
76	00EB	TMRHI	= 0EB				
77	OOEC	TAULO	= 0EC				
78	OOED	TAUHI	= OED				
79	OOEE	CNTRL	= OEE				
80	OOEF	PSW	= OEF				
81	0021	15"	- 011				
82							
			_				
83	0000	GIE	= 0				
84	0001	ENI	= 1				
85	0002	BUSY	= 2				
86	0003	IPND	= 3				
87	0004	ENT I	= 4				
88	0005	TPND	= 5				
89		•••					
90	0002	I EDG	= 2				
91	0003	MSEL	= 3				
92							
	0004	TRUN	= 4				
93	0005	TC3	= 5				
94	0006	TC2	= 6				
95	0007	TC1	= 7				
96							
97							
98		. CHIP	820				
99							
100		; INITIALIZATION	OF REGISTERS	***************			
	0000 DD2F	LD		,			
	0000 BD21	LD	SP,#02F				
102	OUUZ BCEEGU	ស្វ	CNTRL,#080				

```
NATIONAL SEMICONDUCTOR CORPORATION
COP800 CROSS ASSEMBLER, REV: D1, 12 OCT 88
                                                                                                                                                                            :GLOBAL INT ENABLE/EXTINT ENABLE
    103 0005 BCEF03
                                                                                                            PSW. #003
                                                                                                           PSW,#003
PORTGD,#01
PORTGC,#03E
PORTD,#09
PORTLC,#00
PORTLD,#0FF
   104 0008 BCD401
105 000B BCD53E
                                                                                       LD
                                                                                                                                                                            : CONFIG PORTG FOR OUTPUTS
                                                                                                                                                                           ;CONFIG PORTS FOR COTPUTS;
;START MOTOR DRIVE VALUE;
;CONFIG PORTL FOR INPUTS;
;CONFIG PORTL FOR WEAK PULL-UPS
;SETUP MOTOR DRIVE VALUES
   106 000E BCDC09
107 0011 BCD100
108 0014 BCD0FF
109 0017 5F
                                                                                       LD
                                                                                       LD
LD
                                                                                                            B.#MSO
  109 0017 5F
110 0018 9A09
111 001A 9A0C
112 001C 9A06
113 001E 9E03
114 0020 D200
115 0022 BC0700
                                                                                                           B, #MS0

[B+], #09.

[B+], #0C

[B+], #06

[B], #03

STPPTR, #00

FLGREG, #00
                                                                                        LD
                                                                                        LD
                                                                                        LD
                                                                                                                                                                            ; INIT STEP POINTER
; INIT FLAG REGISTER
   116
117
118
                                                                  ; READ, DECODE, AND EXECUTE COMMAND
   119
120 0025 BDD479
                                                                                                                                                                          ; TURN ON READY FOR NEXT CMD LED
;WAIT FOR CMD AND READ CMD
;TURN OFF READY FOR NEXT CMD LED
;STORE IN CMD REGISTER
;IF STOP BIT SET
;THEN STOP MOTOR
;ELSE CHEK MODE
;IF MODE SET THEN GO SINGLE STEP
;ELSE GO CONTINUOUS
;GO WAIT FOR NEXT COMMAND
;STOP THE MOTOR
;TURN OFF ALL LEDS
;GO WAIT FOR NEXT CMD
                                                                                       SBIT
                                                                                                            READY, PORTGD
                                                                  TOP:
   121 0028 3081
122 002A BDD469
                                                                                        JSR
                                                                                                            WAIT
READY, PORTGD
                                                                                        RBIT
   123 002D 9C04
124 002F BD0470
125 0032 08
                                                                                                            A,CMD
GO,CMD
STOP
                                                                                        IFBIT
                                                                                        JP.
  125 0032 08
126 0033 BD0472
127 0036 3041
128 0038 305F
129 003A EA
                                                                                        IFBIT
                                                                                                            MODE, CMD
                                                                                        JSR
                                                                                                            SSTEP
                                                                                        JSR
                                                                                                            CONT
                                                                                        JР
                                                                                                            TOP
    130
                                                                  STOP:
   131 003B 308E
132 003D BCD401
133 0040 E4
                                                                                        JSR
                                                                                                            TMRSET
                                                                                       LD
JP
                                                                                                            PORTGD, #01
   134
135
136
137
                                                                   ; SINGLE STEP THE MOTOR (SS)
                                                                                                                                                                          STOP TIMER
STOP TIMER
TURN ON SS LED (RST ALL OTHER LEDS)
WAIT FOR CMD BYTE 2 (# STEPS)
ADD 1 TO CORRECT FOR LOOP
STORE *STEPS IN LOBYTE COUNT REG
LOAD HIBYTE * STEPS
MOVE TO LOWER NIBBLE
GET RID OF UPPER BITS
ADD 1 TO CORRECT FOR LOOP
MOVE TO HIBYTE OF COUNT REG
DECR LOBYTE AND IF NOT ZERO
THEN GO DO A STEP
ELSE DECR HIBYTE AND IF NOT ZERO
THEN GO DO A STEP
THEN GO DO A STEP AND RST LO COUNT
ELSE END OF LOOP
RETURN
                                                                                                                                                                            138 8 139 0041 308E 140 0043 BCD410 141 0046 3081 142 0048 8A 143 0049 9CF0 144 004B 9D04 145 004D 65 146 004E 950F 147 0050 8A 148 0051 9CF1
   138
                                                                  SSTEP:
                                                                                        JSR
                                                                                                            PORTGD, #010
WAIT
                                                                                        LD
                                                                                        INC
                                                                                                            A, CREGO
                                                                                       X
LD
                                                                                                            A, CMD
                                                                                                            A
A,#0F
                                                                                        SWAP
                                                                                        AND
                                                                                        INC
                                                                                                            A CREG1
   148 0051 9CF1
149 0053 C0
                                                                                        DRSZ
                                                                  TP2:
                                                                                                            CREG0
   150 0054 05
151 0055 C1
                                                                                                            DO
CREG1
                                                                  MID:
                                                                                       DRSZ
   152 0056 01
153 0057 8D
                                                                                        RETSK
```

NATIONAL SEMICONDUCTOR CORPORATION COP800 CROSS ASSEMBLER, REV: D1, 12 OCT 88 CREGO, #OFF ; RESET LOBYTE OF COUNTER 154 0058 DOFF 155 005A 3098 156 005C 3158 157 005E F4 NXTVAL DELAY STEP THE MOTOR SLOW THE STEPPING DO: **JSR** JSR JP GO TO TOP OF LOOP 159 160 ; RUN THE MOTOR CONTINUOUSLY (NS = NON-STOP = CONTINUOUSLY) 161 162 CONT: 163 005F BDEE74 164 0062 01 TRUN, CNTRL CHKSPD ; IF MOTOR ALREADY RUNNING NS; THEN CHECK THE CURRENT SPEED; ELSE GO START THE MOTOR; COMPARE INPUT WITH ACTUAL SPD IFRIT 165 0063 03 166 0064 3148 JP SETGO CHKSPD: JSR SPEED COMPARE INPUT WITH ACTUAL SPD IF EQUAL RET ELSE RESTART MOTOR STOP THE TIMER TURN ON CONTINUOUS LED CALCULATE TIMER (SPEED) VALUE LOAD A WITH TVAL1 MOVE SPEED VAL INTO TIMER LOAD A WITH TVAL1 POINT TO TVAL0 MOVE SPEED VAL INTO AUTORELOAD REG LOAD A WITH TVAL0 MOVE SPEED VAL INTO TIMER LOAD A WITH TVAL0 MOVE SPEED VAL INTO TIMER LOAD A WITH TVAL0 167 0066 8E 168 0067 308E RFT SETGO: JSR 169 0069 BCD420 170 006C 3126 L.D PORTGD, #020 JSR TIMVAL A,[B] A,TMRHI A,[B-] A,TAUHI 171 006E AE 172 006F 9CEB LD 173 0071 AB 174 0072 9CED I.D 175 0074 AE 176 0075 9CEA 177 0077 AE 178 0078 9CEC A,[B] A,TMRLO L.D L.D A,[B] A,TAULO 179 007A BDEF7C 180 007D BDEE7C ENTI, PSW TRUN, CNTRL SBIT :ENABLE TIMER INTERRUPT START THE TIMER RET TO MAIN AND WAIT FOR THRINT 181 0080 8E RET 183 184 185 WAIT: 187 188 0081 BD0770 189 0084 01 190 0085 FB 191 0086 BD0768 192 0089 9DD2 193 008B 96FF OUT: 194 008D 8E RETURN COMMAND IN ACC 195 196 197 :********** TMRSET: ;RESET THE TIMER RBIT TRUN,CNTRL RBIT TPND,PSW RBIT ENTI,PSW 198 199 008E BDEE6C ;STOP THE TIMER ;RESET THE TIMER PENDING BIT ;DISABLE TIMER INTERRUPT 200 0091 BDEF6D 201 0094 BDEF6C 202 0097 8E RET

204

NATIONAL SEMICONDUCTOR CORPORATION COP800 CROSS ASSEMBLER, REV: D1, 12 OCT 88 NXTVAL: 205 ;SEND THE NEXT DRIVE VALUE TO STEP THE MOTOR ONE STEP IN THE ;APPROPRIATE DIRECTION (CW OR CCW) LD A,STPPTR ;LOAD STEP VALUE POINTER LD B,#PORTGD ;POINT TO PORT G IFBIT DIR,CMD ;IF CLOCKWISE 206 207 208 0098 9DF2 209 009A DED4 210 009C BD0471 211 009F 11 212 00A0 6A 213 00A1 7B 214 00A2 8B IPTR CW,[B] CCW,[B] ;THEN GO INCREMENT POINTER; ELSE RST CW LED; TURN ON CCW LED; AND DECREMENT POINTER JP DPTR: RBIT SBIT DEC 214 00A2 8B 215 00A3 92FF 216 00A5 9803 217 00A7 9CF2 218 00A9 9DF2 219 00AB 9CFE ; IF OFF BOTTOM OF STEPS ;THEN LOOP TO TOP OF STEPS ;A -> STPPTR (SAVE NEW STPPTR) ;[STPPTR] -> PORTD (LOOKUP VAL) IFEQ A,#OFF A,#03 A,STPPTR LD WRVAL: LD A, STPPTR A,B A,[B] A,PORTD 220 00AD AE 221 00AE 9CDC 222 00B0 8E 223 00B1 6B ĹD ; WRITE STEP VALUE TO MOTOR RET IPTR: RBIT CCW,[B] ;TURN OFF CCW LED ;TURN OFF CCW LED ;TURN ON CW LED ;INCREMENT THE STEP POINTER ;IF OFF TOP OF STEPS ;THEN LOOP TO BOTTOM OF STEPS ;GO WRITE VALUE TO MOTOR 224 00B2 7A 225 00B3 8A SBIT CW,[B] INC A 226 00B4 9204 227 00B6 64 IFEQ A,#04 CLR 228 00B7 EF 229 WRVAL 230 ; INTERRUPT HANDLERS 231 . = 0FF ;BRANCH TO THE APPROPRIATE INTERRUPT HANDLER TPND PSW ;TIMER UNDERFLOW : ************ 232 OOFF 233 234 00FF BDEF75 234 OUFF BDEF75 235 0102 08 236 0103 BDEF73 237 0106 16 238 0107 BDEF78 239 010A 8D TMRINT 4I. IFBIT IPND, PSW ; EXTERNAL INTERRUPT JP EXTINT SBIT GIE, PSW ; SOFTWARE TRAP RETSK 240 241 242 TMRINT: RESET THE TIMER INTERRUPT PENDING BIT AND STEP THE MOTOR 243 010B 9CF9 244 010D 9DFE 245 010F 9CFA 246 0111 BDEF6D 247 0114 3098 248 0116 9DFA A, 0F9 A, B CONTEXT SAVE ROUTINE LD A, OFA TPND, PSW NXTVAL RBIT :RESET PENDING BIT ;STEP THE MOTOR ;CONTEXT RESTORE ROUTINE JSR A, OFA A, B LD 249 0118 9CFE 250 011A 9DF9 A, OF9 ĹD 251 011C 8F RETI 252 EXTINT:

SRIT

JSR

INT, FLGREG DELAY

254 011D BD0778

255 0120 3158

TL/DD/11044-7

SET INTERRUPT OCCURED FLAG

:WAIT

NATIONAL SEMICONDUCTOR CORPORATION COP800 CROSS ASSEMBLER, REV: D1, 12 OCT 88 256 0122 BDEF6B 257 0125 8F 258 IPND, PSW ; RESET PENDING BIT RETI 259 260 261 262 ; SUPPORT ROUTINES CONTINUED 262 263 264 265 266 267 268 269 :POINT TO STORAGE FOR TIMVAL :IF LOWEST SPEED BIT SET :THEN USE SLOWEST SPEED :IF SECOND LOWEST SPD BIT SET :THEN USE SLOW SPEED :IF SECOND HIGHEST SPD BIT SET :THEN USE FAST SPEED :ELSE USE FASTEST SPEED :400steps/sec = 2rev/sec B. #TVALO 1.D IFBIT JP IFBIT 4,CMD SLOWER 5,CMD SLOW 6,CMD FAST JP IFBIT [B+],#02 [B],#08 FASTER: LD RET FAST: [B+],#088 [B],#013 LD ;200steps/sec = 1rev/sec RET [B+],#018 [B],#027 SLOW: LD ;100steps/sec = .5rev/sec 286 0140 9E27 287 0142 8E 288 0143 9A54 289 0145 9E9C RET SLOWER: LD [B+],#054 [B],#09C ;25steps/sec = .125rev/sec 290 0147 8E 291 RET 292 292 293 294 295 0148 3126 296 014A 9D14 297 014C BDEC82 298 014F 01 299 0150 8D 300 0151 9D15 301 0153 BDED82 302 0156 8E 303 0157 8D SPEED: ********** COMPARE CURRENT MOTOR SPEED WITH DESIRED MOTOR SPEED JSR LD IFEQ TIMVAL A,TVALO A,TAULO TSTHI ; CALCULATED DESIRED SPEED VAL ; IF DESIRED LBYTE EQUALS CURRENT LBYTE ;THEN GO TEST HI-BYTE ;ELSE NOT EQUAL RETURN AND SKIP JΡ RETSK LD IFEQ ;IF HI-BYTE EQUALS CURRENT HI-BYTE ;THEN DESIRED = CURRENT RETURN ;ELSE DESIRED != CURRENT RET & SKIP A. TAUHI RET 303 0157 8D 304 RETSK 305 DELAY: 306 ; INSERT A DELAY

NATIONAL SEMICONDUCTOR CORPORATION COP800 CROSS ASSEMBLER, REV: D1, 12 OCT 88

307 0158 D301 308 015A D4FF 309 015C C4 310 015D FE 311 015E C3 312 015F FA 313 0160 8E 314	DLY1: LD DLY2: DRSZ JP DRSZ JP RET .END	0F3,#01 0F4,#0FF 0F4 DLY2 0F3 DLY1	;FOR SINGLE STEP & EXTINT DEBOUNCE;APPROX .256mS X 6
			TL/DD/11044-9
B 00FE CMD 0004 CREG1 00F1 DLY1 015A DPTR 00A0 * FAST 0139 GO 0000 IPTR 00B1 MS1 0001 * NS 0005 * PORTIC 00D1 READY 0001 SLOWER 0143 SSTEP 0441 TAUHI 00ED TC3 0005 * TMRLO 0064 TPND 0005 TVAL1 0015	BUSY 0002 ** CNTRL 00EE CW 0002 DLY2 015C ENI 0001 ** FASTER 0134 ** IEDG 0002 ** MID 0055 ** MS2 0002 ** NXTVAL 0098 PORTGD 00D4 PORTLD 00D0 SETGO 0067 SP 00FD STEPS 0005 ** TAULO 00EC TIMVAL 0126 TMRSET 008E TRUN 0004 WAIT 0081	CCW 0003 CONT 005F DELAY 0158 DO 005A ENTI 0004 FLGREG 0007 INT 0000 MODE 0002 MS3 0003 * OUT 0086 PORTGP 00D6 * PORTLP 00D2 SIOR 00E9 * SPEED 0148 STOP 003B TC1 0007 * TMRHI 00EB TOP 0025 TSTHI 0151 WRVAL 00A7	CHKSPD 0064 CREGO 00F0 DIR 0001 DO2 0058 EXTINT 011D GIE 0000 IPND 0003 MSO 0000 MSEL 0003 * PORTD 00DC PORTI 00DT * PSW 00EF SLOW 013E SS 0004 * STPPTR 00F2 TC2 0006 * TMRINT 010B TP2 0053 TVALO 0014 X 00FC

MACRO TABLE

NO WARNING LINES

NO ERROR LINES

282 ROM BYTES USED

SOURCE CHECKSUM = 80C0 OBJECT CHECKSUM = 0520

INPUT FILE C:MOTOR.MAC LISTING FILE C:MOTOR.PRN OBJECT FILE C:MOTOR.LM

TL/DD/11044-11

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- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



National Semiconductor National Semiconductor Corporation 2900 Semiconductor Drive P.O. Box 58090 Santa Clara, CA 95052-8090 Tel: 1(800) 272-9959 TWX: (910) 339-9240

National Semiconductor GmbH GmbH Livry-Gargan-Str. 10 D-82256 Fürstenfeldbruck Germany Tel: (81-41) 35-0 Telex: 527649 Fax: (81-41) 35-1

National Semiconductor Japan Ltd.
Sumitomo Chemical
Engineering Center
Bldg. 7F
1-7-1, Nakase, Mihama-Ku
Chiba-City,
Ciba Prefecture 261 Japan Ltd.

Fax: (043) 299-2500

National Semiconductor Hong Kong Ltd. 13th Floor, Straight Block, Ocean Centre, 5 Canton Rd. Tsimshatsui, Kowloon Hong Kong Tei: (852) 2737-1600 Fax: (852) 2736-9960

National Semiconductores Do Brazil Ltda. Do Brazil Ltda. Rue Deputado Lacorda Franco 120-3A Sao Paulo-SP Brazil 05418-000 Tel: (55-11) 212-5066 Telex: 391-1131931 NSBR BR Fax: (55-11) 212-1181 National Semiconductor (Australia) Pty, Ltd. Building 16 Business Park Drive Monash Business Park Nottinghill, Melbourne Victoria 3168 Australia Tel: (3) 558-9999 Fax: (3) 558-9998