# How to Use the NM93C86A Serial EEPROM as a PC/ Laptop Detachable Printer File Memory Card (DPFMC)

## INTRODUCTION

This applications note describes how to build a DPFMC. The card will be designed around a COP888CG microcontroller and four NM93C86A serial EEPROMs. The card will be designed to plug into any standard IBM-PC/Laptop parallel port. Once the card has been installed, the user can download any document (text, graphic, or combination) and print out that document at a later time. The DPFMC will be designed to make the computer think a printer is actually connected by simulating the printer's input port. Once all documents have been sent, the user needs to press the SEND-DOC button once to save the pointer address. Next the user can remove the card and turn its power off. The documents contained in the card's EEPROMs can be stored for hours, days, months, or even years if needed. However, hours will probably be a more realistic time frame. When the user is ready to print-out the documents saved, the card can be plugged into a stand along printer by either using the printer's DB-25 cable or (with an appropriate adapter) the printer itself. After switching on the cards power, all the user has to do is press the SEND DOC button and the printer will begin to print out the saved documents.

#### NM93C86A DESCRIPTION

The NM93C86A is a 16,384-bit CMOS non-volatile serial EEPROM that can be configured to have a 1024 x 16 or a 2048 x 8 architecture. The configuration is determined by the state of the ORG pin. If the ORG pin is tied low the NM93C86A is configured as a 2048-byte-wide memory. If the ORG pin is left floating or tied to V<sub>CC</sub>, the 1024 word wide configuration is enabled. An internal pull-up resistor to V<sub>CC</sub> assures that a floating ORG pin is pulled high. *Figure 1* shows the NM93C86A pin arrangement.

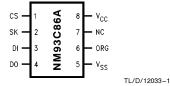


FIGURE 1. NM93C86A Pin Out

The NM93C86A has 7 instructions that can be performed. The instructions are: Read a byte/word (READ), Enable programming (EWEN), Disable programming (EWDS), Erase a byte/word (ERASE), Write a byte/word (WRITE), Erase all bytes/words (ERAL), and write a data pattern to all bytes/words (WALL). The NM93C86A uses the industry standard MICROWIRE™ interface.

#### COP888CG DESCRIPTION

The COP888CG is an 8-bit microcontroller. Its a fully static CMOS device containing RAM, ROM, and Microwire interface. The microcontroller contains 4,096 bytes of ROM used to store program code and 192 bytes of RAM used to

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Detachable Printer File Memory Card (DPFMC

How to

Use

the NM93C86A

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EPROM as a PC/Laptop

store register data. It contains an 8-bit input, an 8-bit output, and two 8-bit bi-directional ports. The microcontroller also has a Microwire interface which will be used to connect the NM93C86A to it. These attributes make the COP888CG a good choice for this particular application.

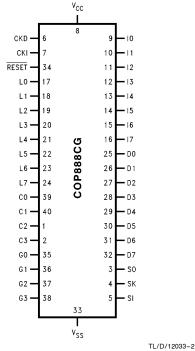


FIGURE 2. COP888CG Microcontroller

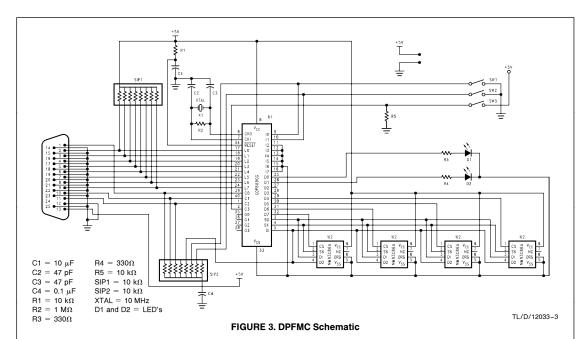
#### CIRCUIT DESCRIPTION

*Figure 3* shows the complete schematic diagram of the DPFMC. The L-port of the microcontroller is connected to the D0–D7 data lines of the computer's bidirectional data port. The LS-nibble of C-port is connected to the STROBE, ACKNLG, BUSY, and the PE pins of the computer's parallel port. Pin 13 is tied HIGH and all of the other pins are tied LOW. All of the bi-directional I/O lines are pulled HIGH through two 10 k $\Omega$  SIP resistors.

The MICROWIRE Interface of the COP888CG uses pins 3(SO), 4(SK), and 5(SI). These pins are connect to the equivalent pins of the EEPROM. The MS-nibble of the D-port is used as chip select outputs to the four EEPROMs. Since the data the DPFMC will be processing is 1-byte length data, the NM93C86A will be configured to deal with 8-bit chunks instead of 16-bit chunks of data.

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The DPFMC will have three inputs. Two are tied HIGH through 10 k $\Omega$  resistors. The third is tied LOW. The first input when LOW (pin 9) will indicate RECEIVE MODE. A LOW on the second input (pin 10) will indicate WRITE

MODE. A HIGH on the third input (pin 11) will tell the microcontroller to save the current pointer address and begin sending the document data to the printer. Their are also two LED outputs. LED1 will inform the user that the DPFMC cannot accept any more data. LED1 also informs the user that the pointer address has been saved. If LED1 flashes continuously the DPFMC has caude the printer.

LED1 flashes irregular the DPFMC has saved the pointer address and is ready for power to be removed. LED2 informs the user of the current mode. Two flashes indicates the READ MODE and one flash indicates the WRITE MODE.

#### SOFTWARE DESCRIPTION

For simplicity and structure the software part of this application will be divided into three parts. The first or main block will monitor the three inputs and decide which mode the DPFMC will enter. The second block will control the interface logic between the computer's parallel port and the DPFMC.

In the case of a read operation, this block of code will start by configuring the STROBE and BUSY pins as inputs and

the ACKNLG pin as an output. The routine then waits for the BUSY pin to fall LOW. When the BUSY pin falls low, the routine will begin monitoring the STROBE pin for a 0.5  $\mu s$  LOW. When this happens, the routine reads the data port and stores that data into accumulator A. After the data is safely stored into accumulator A, the ACKNLG pin will be pulsed LOW for 5  $\mu s$  to inform the computer that the data was received. Control is now passed to the final routine. This routine takes the data from ACC A stores the data into the EEPROMs. This routine basically will control the memory matrix part of the card. After the data is stored into the EEPROMs, control is passed back to the interface routine and the loop continues.

The write sequence will be just the opposite. The BUSY and STROBE pins are first configured to be outputs and the ACKNLG pin configured to be a input. The routine will then wait for the SEND DOC input to go low. When this happens the BUSY will go LOW to indicate the card is about to send a byte to the printer. The memory matrix routine the then stores the first byte of data into accumulator A. After that the interface routine sends that data to the port. The STROBE pin is pulsed LOW for 5  $\mu s.$  From this point on the routine monitors the ACKNLG pin for a 5  $\mu s$  LOW. When a LOW has occurred the routine loops back to the top, fetches the top.

; ASSEMBLY CODE FOE THE DETACHABLE PRINTER FILE MEMORY CARD (DPFMC) ; By Charles Watts .INCLD COP888CG.INC .SECT CODE, ROM, ABS=0 ----- INITIALIZE PORT & REGISTER DATA ------: DLYL = OFO DLYH= 0F1= 0F2ADDL ADDH = 0F3 BYT = 00 HLD = 01 STOLO = 02STOHI = 03STOHLD = 04PORTD, #00 PORTGC, #030 MSEL, CNTRL START: LD LD SBIT SBIT S0, CNTRL PORTLC, #00  $\mathbf{LD}$ LDPORTCC, #0B LDPORTCD, #02 LDB, #PORTCP LAST JSR - - - ------ MAIN ROUTINE -----MAIN: LD A, PORTI ; IFEQ A, #06 ; READ JSR ; IFEQ A, #05 JSR WRITE JP MAIN ----- SUBROUTINES WILL FOLLOW ------PORTLC, #00 ; CONFIGURE PORT PORTCC, #06 ; TO READ MODE PORTCD, #02 ; READ: LD LDLDJSR FLSH2 ;BLINK LED 2 TIMES JSR EWEN LP1: IFBIT 3, [B] JSR SAVE ;WAIT FOR STROBE TO GO LOW IFBIT 0, [B] J₽ LP1PORTCD, #06 ;BRING BUSY HIGH LD;READ PORT AND SAVE IN ACCA LDA, PORTLP A, BYT х PUT JSR ;STORE ACCA IN NVM ΤD PORTCD, #04 ; PULSE ACKNLG NOP NOP LDPORTCD, #06 NOP NOP LDPORTCD, #02 ; JP LP1 PORTLC, #0FF ;CONFIGURE PORT PORTCC, #01 ;TO WRITE MODE PORTCD, #01 ; WRITE: LD LDLD JSR FLSH1 LP2: IFBIT 3, [B] ; LP3 JTP ; TL/D/12033-4

	JP	LP2	;	
LP3:		2, [B]	;WAIT FOR BUSY LOW	
	JP	LP3		
	JSR	GET	GET BYTE FROM	
	LD	A, BYT	; . ) 17.7.4	
	X	A, PORTLD	; NVM.	
	NOP NOP			
	LD	PORTCD, #00		
	NOP	PORICE, #00	; PULSE STROBE	
	NOP		;	
	LD	PORTCD, #01	•	
	NOP	IONICE, HOI	•	
	NOP		:	
	JP	LP3	;	
;			,	
GET:	LD	A, HLD	;SET CS HIGH	
	Х	A, PORTD	;	
	LD	A, ADDH	; SEND OPCOCE AND	
	OR	A, #030	;HI ADDRESS	
	Х	A, SIOR	;	
	SBIT	BUSY, PSW	;	
LP4:	IFBIT	BUSY, PSW	;	
	JP	LP4	;	
	LD	A, ADDL	;SEND LOW ADD	
	Х	A, SIOR	;	
		BUSY, PSW	;	
LP5:		BUSY, PSW	;	
	JP	LP5	;	
	LD	SIOR, #000	;	
		BUSY, PSW	;RECEIVE BYTE	
		BUSY, PSW	;	
		BUSY, PSW	;	
LP6:		BUSY, PSW	;	
	JP	LP6		
	X X	A, SIOR		
	LD	A, BYT PORTD, <b>#</b> 00	<i>i</i>	
	JSR	COUNT	<i>i</i>	
	LD	A, HLD	<i>i</i>	
	IFEQ	A, STOHLD	•	
	JP	SKIP1	•	
	RET	5	•	
SKIP1:		A, ADDL		
	IFEQ	A, STOLO	;	
	JP	SKIP2	;	
	RET		i	
SKIP2:	LD	A, ADDH	;	
	IFEQ	A, STOHI	;	
	JP	ZD	;	
	RET		;	
;				
PUT:	LD	A, HLD	;SET CS HIGH	
	X	A, PORTD		
	LD	A, ADDH	; SEND OPPCOCE AND	
	OR	A, #028	;HI ADDRESS	
	X	A, SIOR		
1.07.	SBIT	BUSY, PSW BUSY, PSW		
LP7:	JP	LP7		
	JE	<u>пе /</u>	;	
			TL/D/12033-5	

	LD X	A, ADDL A, SIOR	;SEND LOW ADD ;
LP8:	SBIT IFBIT	BUSY, PSW BUSY, PSW	; ; ;
	JP LD X	LP8 A, BYT A, SIOR	; ;SEND BYTE ;
LP9:	SBIT IFBIT	BUSY, PSW BUSY, PSW	;;
LP10:	JP IFBIT JP	LP9 SI, PORTGP LP10	; ;
LP11:	IFBIT JP	SI, PORTGP LP12	; ; ;
LP12:	JP LD LD SBIT RBIT LD JSR RET	LP11 PORTD, #00 A, HLD A, PORTD SIOR, #0FF BUSY, PSW BUSY, PSW PORTD, #00 COUNT	; ; ; ; ; ;
; EWEN:	LD LD SBIT	PORTD, #0F0 SIOR, #026 BUSY, PSW	; Enable EE ; ;
LP13:		BUSY, PSW LP13 SIOR, #00	;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;
LP14:	IFBIT JP LD RET	BUSY, PSW LP14 PORTD, #00	; ; ; ;
; COUNT:	LD IFEQ JP INC X RET	A, ADDL A, #0FF ZA A A, ADDL	; Address Counter ; ; ;
ZA:	LD IFEQ JP INC X LD	A, ADDH A, #07 ZC A A, ADDH ADDL, #00	; ; ; ;
ZC:	RET LD IFEQ JP LD ADD X	A, HLD A, #080 ZE A, HLD A, HLD A, HLD A, HLD	, ; ; ; ;
	LD LD RET	ADDL, #00 ADDH, #00	; ; ;
ZE:	JSR	SAVE	; TL/D/12033-6

ZD:	LD JSR LD JSR JP	PORTD, #02 DELAY PORTD, #00 DELAY ZD	; ; ; ;	
; SAVE:	LD X LD LD X LD X LD X JSR LD X JSR LD X JSR IFBI X SR RET	A, ADDL A, STOLO ADDL, #0FD A, ADDH A, STOHI ADDH, #07 A, HLD A, STOHLD HLD, #084 A, STOHL A, BYT PUT A, STOHI A, BYT PUT A, STOHLD A, BYT PUT T 3, [B] GOTIT	; Save Pointer	
GOTIT:	LD JSR LD JSR LD JSR LD JSR JSR JSR JP	PORTD, #02 DELAY PORTD, #00 DELAY PORTD, #02 DELAY PORTD, #00 DELAY DELAY DELAY GOTIT	; LED Flashing sequence ; ; ; ; ; ; ; ; ; ;	
; LAST:	LD LD JSR LD X JSR LD X JSR LD X LD LD LD RET	ADDL, #0FD ADDH, #07 HLD, #084 GET A, BYT A, STOLO GET A, BYT A, STOHI GET A, BYT A, STOHLD ADDL, #00 ADDH, #00 HLD, #010	; Get pointer ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ;	
; FLSH2:	LD JSR LD	PORTD, #001 DELAY PORTD, #00	;FLASH LED 2 TIMES ; ;	

JSR DELAY ; PORTD, #001 ; LD JSR DELAY ; PORTD, #00 LD; RET ; , FLSH1: LD PORTD, #001 ;FLASH LED ONCE JSR LD DELAY PORTD, #00 DELAY ; ; JSR ; RET ; ; DELAY: LD DLYH, #040 LP15: LD DLYL, #0FF ; ; LP16: DRSZ DLYL ; JP LP16 DRSZ DLYH ; ; JP LP15 ; RET ; .END START ; TL/D/12033-8

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