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0000 |           ; Inputs:  Checks for keyboard input from the user.  Currently, the following
0000 |           ;         key sequences are honored if input after the system "clicks" the
0000 |           ;         speaker (CMD refers to the Apple key on the keyboard):
0000 |           ;
0000 |           ;         CMD/1 - boot from Twiggy drive #1 or integral hard disk
0000 |           ;         CMD/2 - boot from Twiggy drive #2 or SONY drive
0000 |           ;         CMD/3 - boot from Profile attached to parallel port or integral hard disk
0000 |           ;         CMD/4 - boot from I/O slot #1, port 1
0000 |           ;         CMD/5 - boot from I/O slot #1, port 2
0000 |           ;         CMD/6 - boot from I/O slot #2, port 1
0000 |           ;         CMD/7 - boot from I/O slot #2, port 2
0000 |           ;         CMD/8 - boot from I/O slot #3, port 1
0000 |           ;         CMD/9 - boot from I/O slot #3, port 2
0000 |           ;         CMD/ENTER (on key pad) - abort boot, branch to ROM monitor
0000 |           ;         CMD/SHIFT/P - abort boot, do power cycling
0000 |           ;
0000 |           ; OUTPUTS: Saves various results and contents of system registers in memory
0000 |           ;         for examination by system programs or with the ROM monitor.
0000 |           ;
0000 |           ;         $180-183 : Power-up status (x0000000 = ok)
0000 |           ;         $184-185 : Memory sizing error results
0000 |           ;         $186-1A5 : Results of memory read/write tests
0000 |           ;         $1A6-1A9 : Parity error memory address (if error during mem test)
0000 |           ;         $1AA-1AB : Memory error address latch
0000 |           ;         $1AC-1AF : D7 save on exception errors
0000 |           ;         $1B0-1B1 : Results of MMU tests (context/data bits)
0000 |           ;         $1B2      : Keyboard ID (00 = no ID received)
0000 |           ;         $1B3      : Boot device ID
0000 |           ;         $1B4-1B9 : Boot failure data
0000 |           ;         $1BA-1BF : Clock setting (Ey, dd, dh, hm, ms, st)
0000 |           ;         $1C0-1DF : Data reg save area (D0 - D7)
0000 |           ;         $1E0-1FF : Address reg save area (A0 - A7, A7 = USP)
0000 |           ;         $240-260 : System serial #
0000 |           ;         $260-267 : Scratch area
0000 |           ;         $268-26B : Suspected (logical) memory error address for parity error
0000 |           ;         $26C-26F : Save of data written to suspected error address
0000 |           ;         $270-273 : Actual (logical) error address found during search
0000 |           ;         $274-277 : Save of data read during parity error search
0000 |           ;         $278-27B : (Physical) error address read from parity error address latch
0000 |           ;         $27C      : Error row for parity chip failure (0 = first row, 7 = last row)
0000 |           ;         $27D      : Error column for parity chip failure (9 or 14)
0000 |           ;         $27E-280 : Reserved
0000 |           ;         $280-293 : Exception data save area
0000 |           ;         (FC/EXCADR/IR/SR/PC/EXCTYPE/SSP)
0000 |           ;         44 = NMI or other interrupt
0000 |           ;         45 = bus error
0000 |           ;         46 = address error
0000 |           ;         47 = other exception/interrupt
0000 |           ;         48 = illegal instruction error
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0000 :                               49 = line 1010 or 1111 trap
0000 :                               50 = bus error accessing keyboard VIA
0000 :                               51 = bus error accessing parallel port VIA
0000 :                               57 = bus error accessing disk controller
0000 :                               $294-297 : Maximum physical memory address + 1
0000 :                               $298-299 : I/O slot 1 card id (0 = no card present)
0000 :                               $29A-29B : I/O slot 2 card id
0000 :                               $29C-29D : I/O slot 3 card id
0000 :                               $29E      : Reserved
0000 :                               $29F      : Reserved
0000 :                               $2A0      : Reserved
0000 :                               $2A1      : Disk ROM id
0000 :                               $2A2-2A3 : Reserved
0000 :                               $2A4-2A7 : Minimum physical address
0000 :                               $2A8-2AB : Total memory (Max-Min)
0000 :                               $2AC      : SCC test results
0000 :                               $2AD      : Slot # of memory board if memory error
0000 :                               $2AE      : Result of disk controller self-test
0000 :                               $2AF      : System type (0 = Lisa 1, 1 = Lisa 2, 2 = Lisa 2 with external hard disk,
0000 :                                       3 = Lisa 2 with internal hard disk)
0000 :                               $2B0-2BF : Keyboard queue (16 bytes)
0000 :                               $2C0-480 : ROM scratchpad/stack area
0000 :                               $480-800 : Reserved for ROM local variable usage
0000 :
0000 :                               Also saves data in special parameter memory area reserved for boot ROM use if error
0000 :                               encountered. Usage is as follows:
0000 :
0000 :                               SFCC161      : Error code
0000 :                               SFCC163-165 : Contents of memory error address latch if parity error
0000 :                               SFCC167      : Memory board slot # if memory error
0000 :                               SFCC169-173 : Last value read from clock
0000 :                               SFCC175-17B : Reserved
0000 :                               SFCC17D-17F : Checksum
0000 :
0000 :                               Originator: Rich Castro 7/30/81 - Version 0.0 released to manufacturing
0000 :                               Modified by: Rich Castro 7/30 - 11/3/81 - Made the following changes:
0000 :                                       1) Twiggy bootstrap capability
0000 :                                       2) Initial COPS test and keyboard scan
0000 :                                       3) Moved parallel card to slot 2
0000 :                                       4) Changed ROM interrupt/exception vectors,
0000 :                                       5) Created jump table for ROM routines
0000 :
0000 :                               11/3/81 - Version 0.7 released to the world
0000 :
0000 :                               11/4/81 - 1/15/82 - Made the following changes:
0000 :                                       1) Added support for new memory cards
0000 :                                       2) Added warm-start capability and jump
0000 :                                       table for ROM subroutine usage
0000 :                                       3) Modified MMU reset routine to support
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0000 |      :      single step board usage
0000 |      :      4) Added full memory initialization
0000 |      :      5) Added 256K memory parity test
0000 |      :      6) Modified COPS initialization so that
0000 |      :      keyboard commands can be sensed more
0000 |      :      reliably
0000 |      :      7) Added error code display routines and
0000 |      :      display of CPU and IO ROM versions
0000 |      :      8) Added preliminary disk controller test
0000 |      :      9) Updated warm-start check
0000 |      :      10) Modified disk interface test
0000 |      :      11) Changed low memory assignments
0000 |      :      12) Made corrections for no I/O board, disk
0000 |      :      interface error and contrast setting
0000 |      :      13) Modified memory sizing routine to
0000 |      :      catch memory errors
0000 |      :      14) Modify MMU test to avoid context 0
0000 |      :      destruction, add contrast setting for
0000 |      :      new machines, correct disk error and
0000 |      :      CPU ROM messages
0000 |      :      15) Move stack so old memory test still runs
0000 |      :
0000 |      :      1/15/82 - Release version 0.16
0000 |      :      1/18/82 - Fix stack problem and release vrsn 0.17
0000 |      :      1/19/82 - Change stack for call routine and version
0000 |      :      to 0.18
0000 |      :      1/27/82 - Change MMU error routine to do address
0000 |      :      and data line toggling
0000 |      :      1/28/82 - Add video circuitry test
0000 |      :      1/30/82 - Add write wrong parity test
0000 |      :      1/31/82 - Move run time stack to $180
0000 |      :      2/6/82 - Add Profile bootstrap with upgrade for
0000 |      :      OS use (add jump table entries also)
0000 |      :      2/15/82 - Update Twiggy bootstrap and add entry
0000 |      :      for OS use; also add MMU test to
0000 |      :      conditional assembly and add context
0000 |      :      saving to MMUTST2 routine
0000 |      :      2/17/82 - Add correction to memory test for
0000 |      :      reboot problem and leave parity on
0000 |      :      2/24/82 - Add code for clock test and special
0000 |      :      burn-in cycling
0000 |      :      2/25/82 - Add code to simulate soft on switch
0000 |      :      pressed for COPS problem
0000 |      :      3/1/82 - Removed all changes since ROM 0.18
0000 |      :      release except for parity enabling,
0000 |      :      no reset feature, memory sizing change
0000 |      :      and Profile booting
0000 |      :      3/1/82 - Restore default stack ptr loc to $300
0000 |      :      3/1/82 - Move default stack to $0400, restore
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0000 |      :      Change default boot for new Twiggy code
0000 |      :      to upper Twiggy.  Add conditionals for
0000 |      :      Apple code.
0000 |      :      7/29/82 - Add SCC test, optimize code.
0000 |      :      7/30/82 - Add RAM address uniqueness test.
0000 |      :      8/4/82  - Added the following:
0000 |      :      1)Twiggy mods for interleave
0000 |      :      2)Monitor options CONTINUE and LOOP
0000 |      :      3)Exception routine for line 1111 and
0000 |      :      line 1010 errors.
0000 |      :      8/9/82  - Add Twiggy mod for disk clamp, add mods
0000 |      :      for kernel test failures such as screen
0000 |      :      flash on MMU error.
0000 |      :      8/11/82 - Add memory sizing fix, increase delay
0000 |      :      for COPS and change default boot to
0000 |      :      TWIGGY!!
0000 |      :      8/12/82 - Begin code changes for new user interface
0000 |      :      and add hooks for icon display.
0000 |      :      8/14/82 - Add mods for Twiggy changes to monitor
0000 |      :      DSKDIAG line and add initial timeout.
0000 |      :      Continue user interface changes.
0000 |      :      8/18/82 - Add mouse, cursor code and changes for
0000 |      :      8/23/82 - Add controls for 2716 version of ROM.
0000 |      :      Add changes for Service mode to use
0000 |      :      pull down menu, eliminate keyboard
0000 |      :      queuing while awaiting input.
0000 |      :      8/24/82 - Add dialog box, and window to service
0000 |      :      mode with modified scroll and character
0000 |      :      output routines.
0000 |      :      8/25/82 - Add icons along with routines to display
0000 |      :      during test and for errors.
0000 |      :      8/27/82 - Add routines for displaying and using
0000 |      :      boot icon menu.
0000 |      :      8/30/82 - Add auto boot from Applenet.
0000 |      :      8/31/82 - Add minor additions to Service mode
0000 |      :      for Set and Loop options.
0000 |      :
0000 |      :      8/31/82 - Create and do internal release of
0000 |      :      2716 (0.24), 2732 (1.15) and 2764 (2.00)
0000 |      :      ROM versions.
0000 |      :
0000 |      :      9/8/82  - Add fixes for I/O slot icon display
0000 |      :      and Profile icon display.
0000 |      :      9/9/82  - Add fix for reboot problem in 2716 ROM.
0000 |      :      Add serial # read routine and test for
0000 |      :      2732 and 2764 ROM versions.  Expand
0000 |      :      stack for serial read routine.
0000 |      :      9/10/82 - Add fix for device code display for ROM
0000 |      :      versions 0.24 and 1.15.
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0000 |      :      1) Setting of VIA PCR reg for later use
0000 |      :      2) Reset of keyboard before boot
0000 |      :      3) Fix for slot 3 card check for boot menu
0000 |      :      Add:
0000 |      :      1) Move Profile cmd buffer to location $304
0000 |      :      2) Change default boot device to Profile
0000 |      :      3) Remove support for third boot port on
0000 |      :      each slot
0000 |      :      4) Expand id range for test card search
0000 |      :      5) Don't display Restart button after boot
0000 |      :      error
0000 |      :      6) New icons
0000 |      :      12/18/82 - Fix memory test bug
0000 |      :
0000 |      :      1/3/83 - Fix bug in reporting parity circuitry
0000 |      :      failure. Change version to 2.10.
0000 |      :      1/7/83 - Make following changes:
0000 |      :      1) Change keyboard sequences for I/O slot
0000 |      :      booting
0000 |      :      2) Extend timeout for initial Profile check
0000 |      :      Change SCC test to use max baud rate for
0000 |      :      loopback test
0000 |      :      1/11/83 -
0000 |      :      1/12/83 - Add running of expansion card status routines
0000 |      :      when configuration check is done
0000 |      :      1/18/83 - Add fixes for:
0000 |      :      1) Continuing after memory error
0000 |      :      2) Checking for no reset function
0000 |      :      4) Read of I/O slot ROM for icon data -le 2 meg
0000 |      :      ensure odd address for icon count
0000 |      :      5) Default boot setting when loop on memory
0000 |      :      test selected
0000 |      :      1/21/83 - Add save of disk controller self-test status
0000 |      :
0000 |      :      1/28/83 - Create and release ROM version 2.11
0000 |      :
0000 |      :      3/15/83 - Extend Profile timeout for case where drive
0000 |      :      may be parking head. (bug RM016)
0000 |      :      4/20/83 - Add fixes for:
0000 |      :      1) Memory sizing (bug RM015).
0000 |      :      2) Garbage sent out serial port (RM014).
0000 |      :      3) Removed 6504 (bug RM013).
0000 |      :      4) Never ready Profile (bug RM011).
0000 |      :      Also do some code optimization in icon
0000 |      :      routines to make room for fixes. (RM000)
0000 |      :      4/22/83 - Do code optimization for setting bus error
0000 |      :      vector (labeled as RM000).
0000 |      :      Add changes for following requests:
0000 |      :      1) Display ROM id's on bootup (CHG001)
0000 |      :      2) Loop on address 1Meg-2 if sizing error (CHG002)
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0000 |      :      3) Turn off contrast before doing poweroff (CHG003)
0000 |      :      4) Change copyright notice. (CHG005)
0000 |      :      Also modify alert msg display routine (CHG005).
0000 |      : 4/26/83 - Add loop on CPU diags if no memory or I/O
0000 |      :          board installed. Also toggle LED. (CHG004)
0000 |      : 4/27/83 - Do only basic memory test on warm-start. (CHG006)
0000 |      :          Add fix for NMI bug (RM010).
0000 |      : 5/9/83  - Made following changes:
0000 |      :     1) Change ROM id display to rev # (D) (CHG001)
0000 |      :     2) Change ROM test failure to loop at fixed address
0000 |      :        S00FE00C8 (end of jump table) (CHG007)
0000 |      :     3) Make correction for screen not cleared when
0000 |      :        continuing from I/O slot error to boot menu.
0000 |      :        (CHG008)
0000 |      : 5/10/83 - Add change to enable display of uncompressed icons
0000 |      :          upon external entry to ROM Monitor (CHG008).
0000 |      :
0000 |      : 5/12/83 - Create and release rev D of boot ROM.
0000 |      :
0000 |      : 8/8/83  - Add changes for Pepsi system: (CHG009)
0000 |      :     1) New icons.
0000 |      :     2) Display of icons with id #'s.
0000 |      : 8/9/83  - Add save of disk ROM id in low memory. (CHG010)
0000 |      :          Add fixes for:
0000 |      :     1) SCC init for Applebus. (CHG011)
0000 |      :     2) Test card boot search. (CHG012)
0000 |      : 8/10/83 - Delete inverse video check. (CHG013)
0000 |      :          Add fix to beep routine. (CHG014)
0000 |      : 8/16/83 - Delete memory address and ping pong routines.
0000 |      :          add routines to decode parity error to
0000 |      :          chip. (CHG015)
0000 |      : 9/1/83  - Add retry for hard disk booting. (CHG016)
0000 |      :          Add jump table entry for write to
0000 |      :          parameter memory routine. (CHG017)
0000 |      : 9/2/83  - Add new font, modify display routines. (CHG018)
0000 |      :          Add wait for hard disk ready when
0000 |      :          power-cycling. (CHG019)
0000 |      : 9/6/83  - Add setting of video latch whenever boot
0000 |      :          error causes jump to ROM low memory default
0000 |      :          vectors. (CHG020)
0000 |      :          Add fix for memory test/initialization
0000 |      :          bug. (CHG021)
0000 |      : 9/7/83  - Add read of disk controller ROM self-test
0000 |      :          results. (CHG022)
0000 |      :          Add skip of disk eject on power-off if any
0000 |      :          disk controller errors occurred. (CHG023)
0000 |      :
0000 |      : 9/8/83  - Release for testing (rev 3B) with Pepsi systems.
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