

Mask Set Errata 2

68HC705P9 8-Bit Microcontroller Unit

INTRODUCTION

This errata provides serial I/O port status register (SSR) data collision (DCOL) bit information applicable to the following 68HC705P9 MCU mask set devices:

- D54E

MCU DEVICE MASK SET IDENTIFICATION

The mask set is identified by a four-character code consisting of a letter, two numerical digits, and a letter (e.g., D54E). Slight variations to the mask set identification code may result in an optional numerical digit preceding the standard four-character code (e.g., 1D54E).

MCU DEVICE DATE CODES

Device markings indicate the week of manufacture and the mask set used. The data is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. The date code "9115" would indicate the 15th week of the year 1991.

MCU DEVICE PART NUMBER PREFIXES

Some MCU samples and devices are marked with an "SC" or "XC" prefix. An "SC" prefix denotes special/custom device. An "XC" prefix denotes device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the "MC" prefix.

Whenever contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.

Specifications and information herein are subject to change without notice.



INADVERTENT SETTING OF SSR DATA COLLISION BIT

Description	The serial I/O port status register (SSR) data collision (DCOL) bit (bit 6 of address location \$000B) may be inadvertently set when a serial transmission is initiated in the master mode of operation.
Cause	A race condition exists in the serial I/O port (SIOP) logic that determines when a data collision has occurred.
Solution	Do not read the serial I/O port status register (SSR) data collision (DCOL) bit (bit 6 of address location \$000B) when the SIOP is configured for master mode.

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