

Mask Set Errata 1

68HC705V8 8-Bit Microcontroller Unit

INTRODUCTION

This errata provides information pertaining to latchup and electrostatic discharge (ESD) data applicable to the following 68HC705V8 MCU mask set device:

- 2F82W

MCU DEVICE MASK SET IDENTIFICATION

The mask set is identified by a four-character code consisting of a letter, two numerical digits, and a letter (e.g., F82W). Slight variations to the mask set identification code may result in an optional numerical digit preceding the standard four-character code (e.g., 2F82W).

MCU DEVICE DATE CODES

Device markings indicate the week of manufacture and the mask set used. The data is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. The date code "9115" would indicate the 15th week of the year 1991.

MCU DEVICE PART NUMBER PREFIXES

Some MCU samples and devices are marked with an "SC" or "XC" prefix. An "SC" prefix denotes special/custom device. An "XC" prefix denotes device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the "MC" prefix.

Whenever contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.

Specifications and information herein are subject to change without notice.



HOT LATCHUP/ROOM LATCHUP

Hot latchup fails on PE2/AN10 and PB7/TCAP when a high pulse of 100 mA is applied with all input/output (I/O) held low. The rest of the pins pass hot latchup at 100 mA.

Room latchup fails on PE2/AN10 when all I/O are held low and a high pulse of 175 mA is applied. The rest of the pins pass room latchup at 175 mA.

ELECTROSTATIC DISCHARGE (ESD)

When 2 kV Human Body Model (HBM) ESD is performed, V_{REFH} and PD2 fail leakage according to the specification. All pins pass 1.5 kV HBM ESD and 200 V Machine Model (MM) ESD.

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