

# *Mask Set Errata 1*

## **68HC705MC4 8-Bit Microcontroller Unit**

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### **INTRODUCTION**

This errata provides information concerning the PWM duty cycle transition anomaly on these 68HC705MC4 MCU mask set devices:

- 0F21S
- 1F21S

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### **MCU DEVICE MASK SET IDENTIFICATION**

The mask set is identified by a four-character code consisting of a letter, two numerical digits, and a letter (for example, F21S). Slight variations to the mask set identification code may result in an optional numerical digit preceding the standard four-character code (for example, 1F21S).

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### **MCU DEVICE DATE CODES**

Device markings indicate the week of manufacture and the mask set used. The data is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. The date code "9115" would indicate the 15th week of the year 1991.

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### **MCU DEVICE PART NUMBER PREFIXES**

Some MCU samples and devices are marked with an "SC" or "XC" prefix. An "SC" prefix denotes special/custom device. An "XC" prefix denotes device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the "MC" prefix.

*Whenever contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.*

Specifications and information herein are subject to change without notice.



