

Mask Set Errata 2

68HC705SJ7 8-Bit Microcontroller Unit

INTRODUCTION

This mask set errata provides information pertaining to the serial input/output ports applicable to these XC68HC705SJ7 MCU mask set devices:

- 0H70H, 1H70H

MCU DEVICE MASK SET IDENTIFICATION

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 1H70H. Slight variations to the mask set identification code may result in an altered version number, for example 2H70H.

MCU DEVICE DATE CODES

Device markings indicate the week of manufacture and the mask set used. The date is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code "9115" indicates the 15th week of the year 1991.

MCU DEVICE PART NUMBER PREFIXES

Some MCU samples and devices are marked with an SC or XC prefix. An SC prefix denotes special/custom device. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC prefix.

When contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.

Specifications and information herein are subject to change without notice.



SERIAL INPUT/OUTPUT PORT (SIOP) INTERRUPT

- Refer to these two items regarding the SIOP interrupt.
- Item #1 In slave mode, the SIOP fails to set the serial port interrupt flag (SPIF) when a byte of data is received. Also in slave mode, the SIOP fails to assert an interrupt if the serial peripheral interrupt enable (SPIE) bit in the SIOP control register (SCR) is set.
- Item #2 In slave mode, if the CPU stacks because the analog subsystem asserts a CPU interrupt during this time, then the CPU loads its program counter (PC) with the vector address stored in memory locations \$1FF0 and \$1FF1, which do not contain interrupt vectors and are, therefore, indeterminate.

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters which may be provided in Motorola data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and  are registered trademarks of Motorola, Inc. Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

Additional mask set erratas can be found on the World Wide Web at <http://Design-NET.com/csic/TECHSPRT/TechSprt.htm>.



MOTOROLA

68HC705SJ7MSE2