

Mask Set Errata 3 **68HC705V12 Microcontroller Unit**

INTRODUCTION

This mask set errata provides information concerning J1850 network out-of-range flag reception in 4X receive mode on these 68HC705V12 MCU mask set devices:

- 0H52P
- 0G96Y, 1G96Y, 2G96Y, 3G96Y
- 0G78M

MCU DEVICE MASK SET IDENTIFICATION

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 0G96Y. Slight variations to the mask set identification code may result in an altered version number, for example 1G96Y.

MCU DEVICE DATE CODES

Device markings indicate the week of manufacture and the mask set used. The data is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code "9115" indicates the 15th week of the year 1991.

MCU DEVICE PART NUMBER PREFIXES

Some MCU samples and devices are marked with an SC or XC prefix. An SC prefix denotes special/custom device. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC prefix.

When contacting a Motorola representative for assistance, please have the MCU device mask set and date code information available.

Specifications and information herein are subject to change without notice.



4X MODE INVALID SYMBOL FLAGS

If the BDLC is placed into 4X receive mode by writing a \$20 to BCR2, then an idle bus longer than 70 μ s can generate multiple invalid symbol flags in the BSVR. The 4X mode incoming message is received correctly if the invalid symbol flags are cleared.

Software Workaround — Clear the invalid symbol flags by servicing the BSVR interrupts.

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